



Parking Lot Availability System using a Campus-Wide Wireless Network

MATTHEW COOK, NIKITA EISENHAUER, STEPHANIE FOURNIER, WARREN SETO

ADVISOR: DR. PEARLSTEIN

PROBLEM

- Busy places often have busy parking lots
- Drivers spend lots of time driving around looking for a place to park
- Lots of time driving around leads to lots of extra emissions being sent into the atmosphere
- Leads to driver frustration and traffic congestion

PRIOR ART

Google Maps



Street Line



PocketParker, a research project from the State University of New York at Buffalo



Los Angeles Express Park

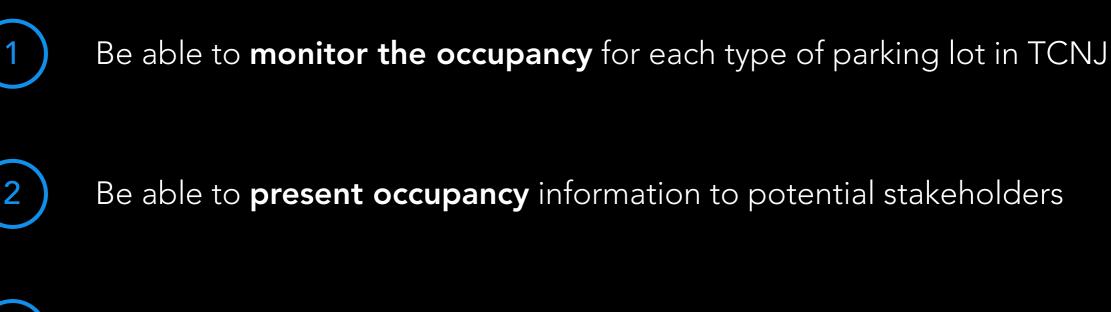


Disney Theme Parks



Previous Senior Projects

REQUIREMENTS

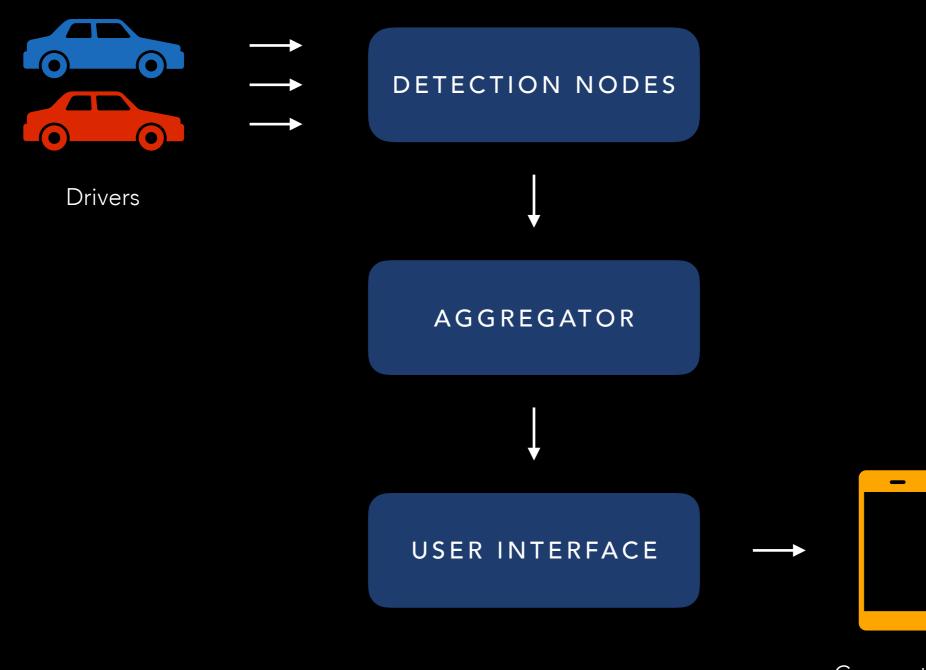


Abide by TCNJ's Privacy and Tracking Policy

4 Scalable

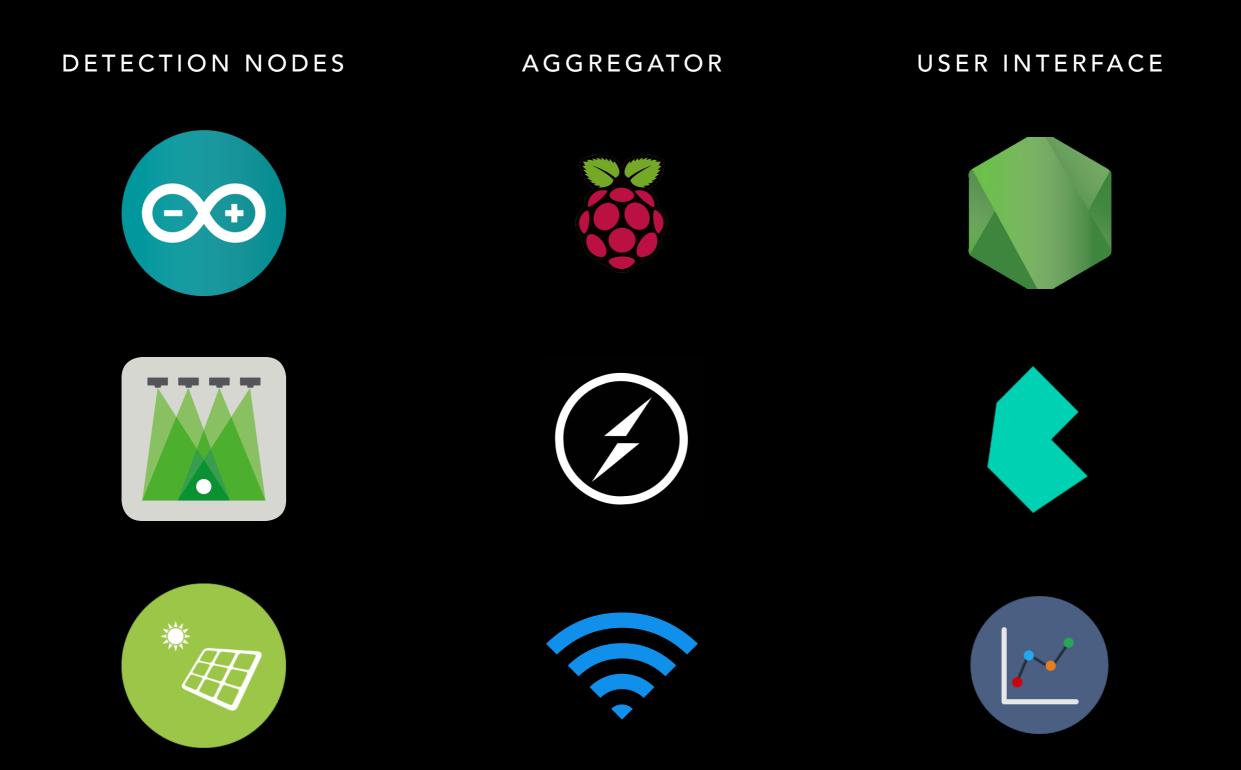
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GENERAL ARCHITECTURE



Commuters

PLANNED TECHNOLOGIES



SPECIFICATIONS

AGGREGATOR USER INTERFACE **DETECTION NODES** Raspberry Pi B+ Amazon Web Services (AWS) ATmega32u4 @ 8MHz VL53L0x LiDar Time of Flight Sensor Raspbian Stretch Light nodeJS 8 & ECMAScript 2015 300 MHz to 1100 MHz SemTech 915MHz LoRa Radio PostgreSQL 9.5.3 Telescopic Antenna Modern HTML5 and CSS Flexbox 3.7V 4000 mAh LiPoly Battery Socket.IO v2 LoRaWAN Statistical Analysis 2.5W 5V/500mAh Solar Panels

ENGINEERS









Matthew Cook Computer Engineer

Detection

Nikita Eisenhauer Electrical Engineer

Power System

Stephanie Fournier Electrical Engineer

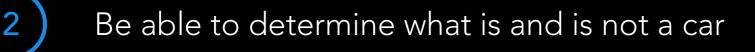
Enclosure & Assembly

Warren Seto Computer Engineer

Base Station & Server

DETECTION REQUIREMENTS







Low Power Consumption



5

Transmit reliable data to the aggregator



DETECTION SCENARIO





- 1. Car Drives Into Lot
- 2. Sensor 1 Detects Car
- 3. Sensor 2 Detects Car
- 4. Node tells Aggregator that a Car has Entered the Lot

POWER SYSTEM SPECIFICATION

Charges battery in under 5 hours

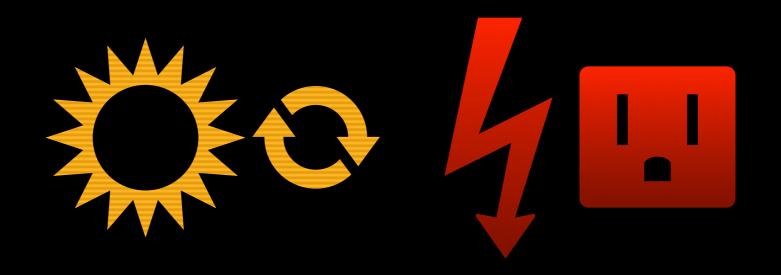


Capable of powering board and charging battery simultaneously



Run for approximately 5 days solely on battery power (assuming a typical traffic scenario)

POWER SYSTEM SCENARIO



- 1. Solar energy converted to electricity via solar panel
- 2. Electricity regulated, controlled and diverted as necessary
- 3. Battery charges during day when sunlight available and discharges at night or when sunlight is unavailable

WIRELESS SPECIFICATION

Long Range - up to half a mile of range

2

Power Efficient - +5 to +20 dBm up to 100 mW Power Output Capability

3

Reliable Datagram - Addressed, acknowledged variable length messages

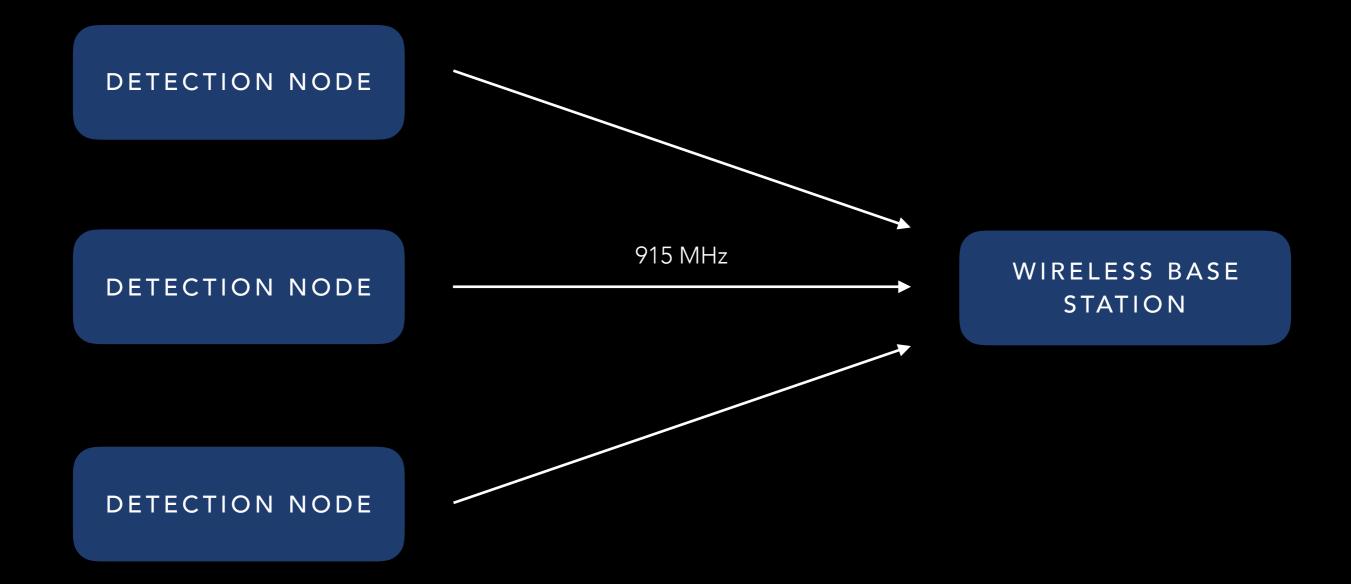
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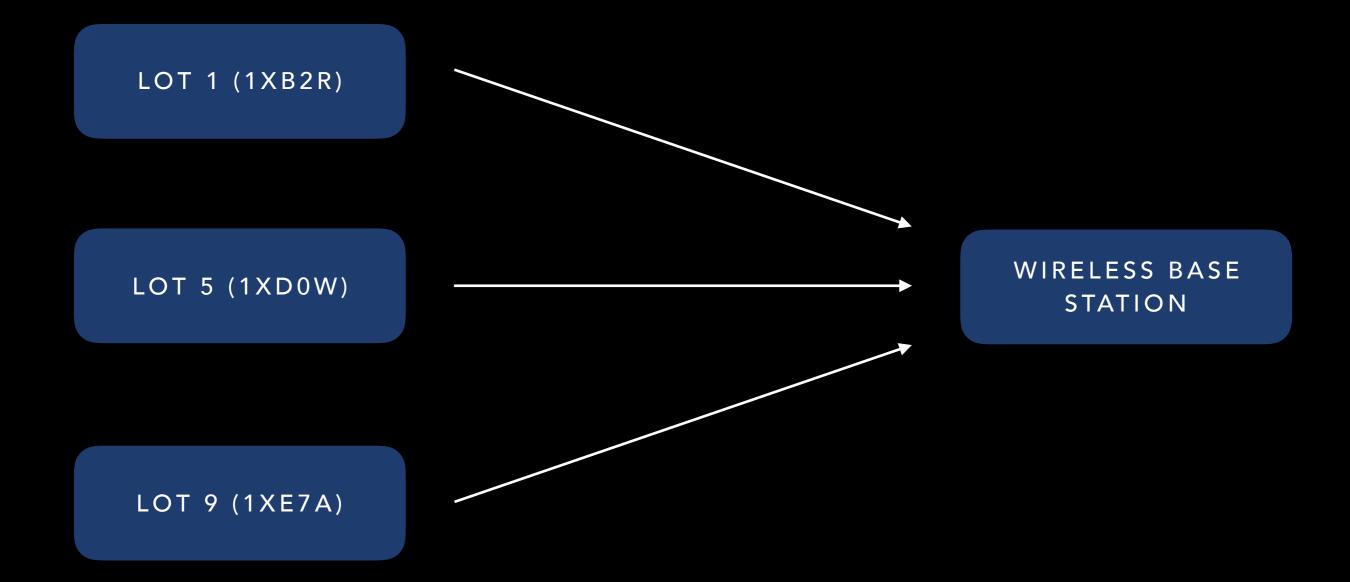
Secure - Advanced Encryption Standard (AES)

License Free - 915 MHz ISM Band for ITU "Americas"

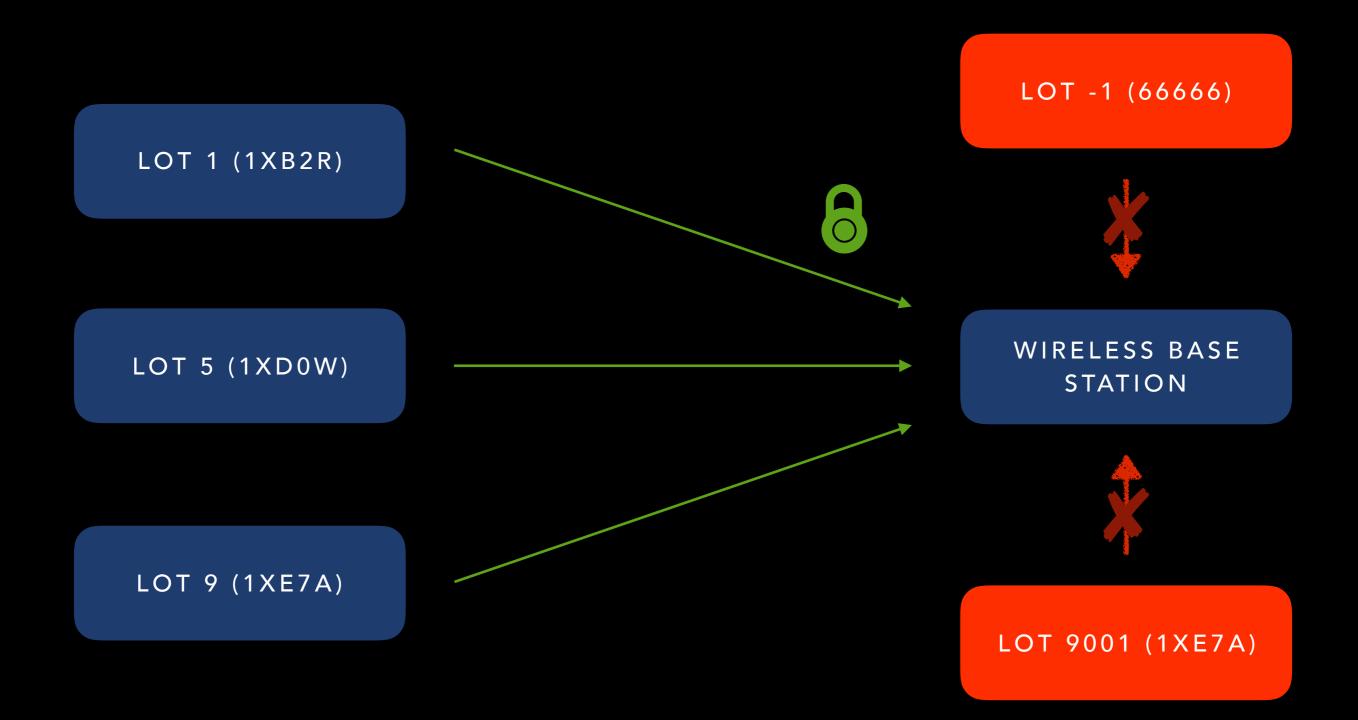
WIRELESS ARCHITECTURE



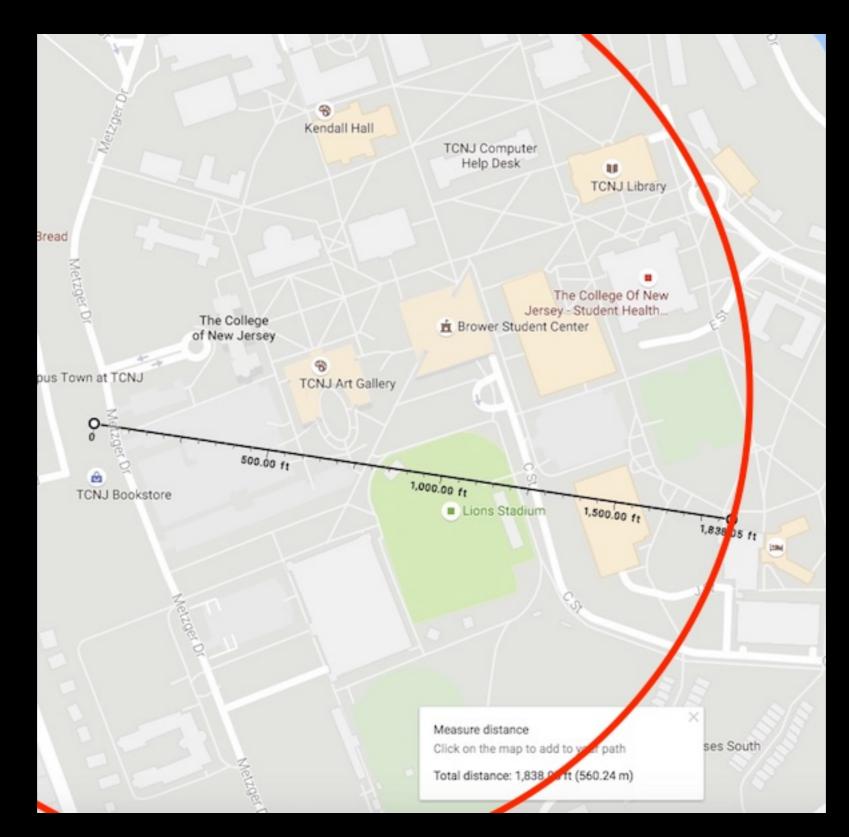
WIRELESS ARCHITECTURE



WIRELESS ARCHITECTURE



WIRELESS RANGE TEST

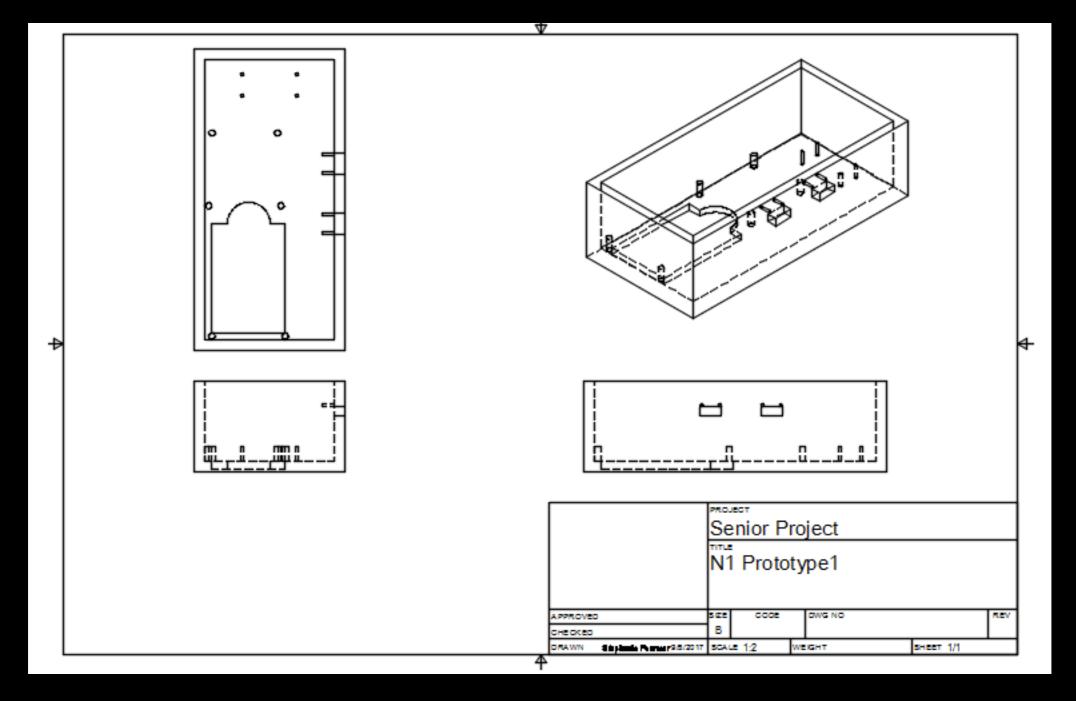


ENCLOSURE SPECIFICATION

There will be two different designs for cases to enclose microcontrollers. (Two cases for the two detection nodes and one enclosure for the aggregator.)

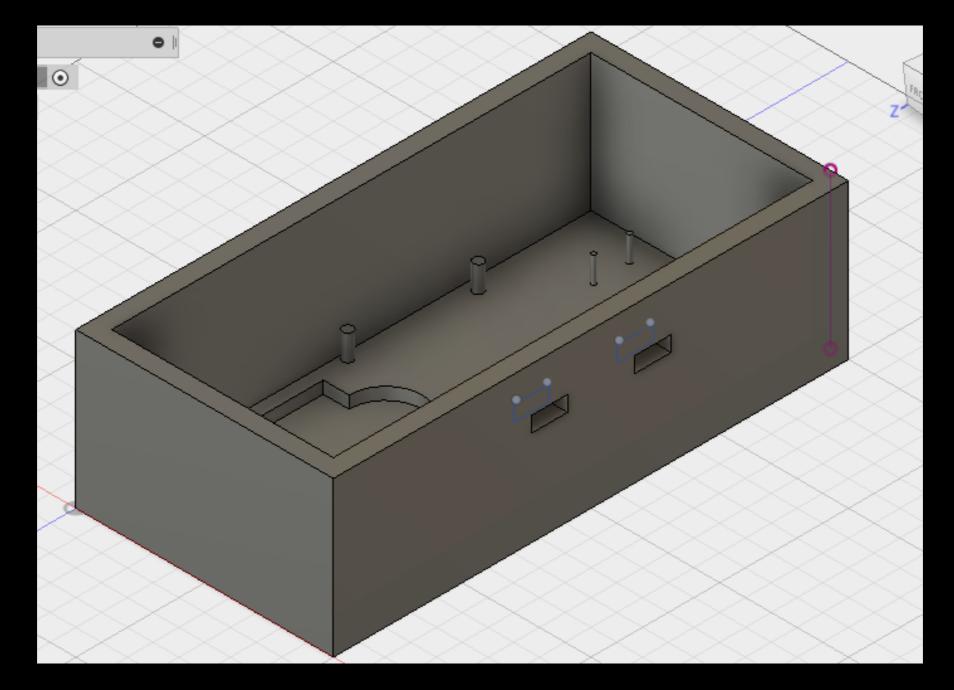
Pillars will be made within the cases to secure the placement of the microcontrollers. There will also be a groove on the surface so that the solar panel and battery can be placed.

ENCLOSURE DRAWING



Prototype 1

ENCLOSURE RENDER



Prototype 1

FRONTEND SPECIFICATION



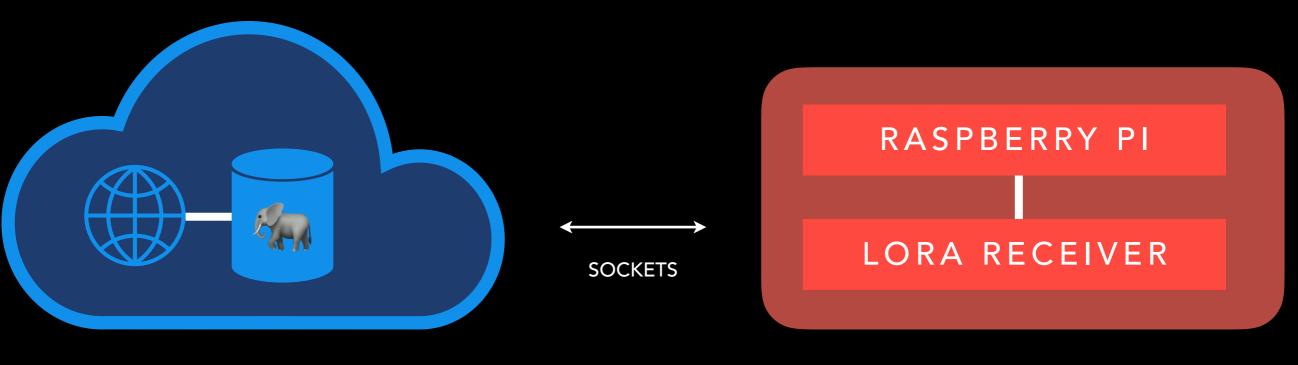
Present Occupancy Information - HTML5 & CSS Flexbox



4

Response Time - Under 10 ms per request

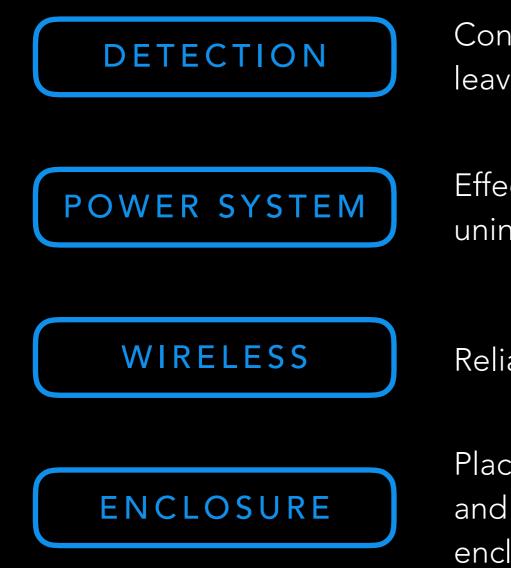
FRONTEND SPECIFICATION



AWS

Wireless Base Station

VERIFICATION



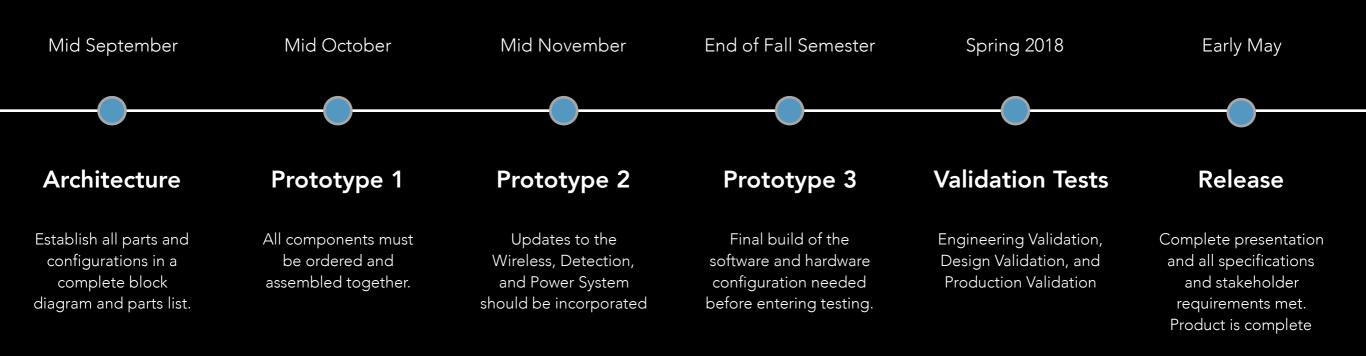
Consistently and Reliably detect cars entering and leaving the parking lot

Effectively regulate and distribute continuous, uninterrupted power to all modules

Reliability Stress Test and Coverage Check

Place the components inside to ensure a proper fit and that the components work efficiently within the enclosure.

ROADMAP



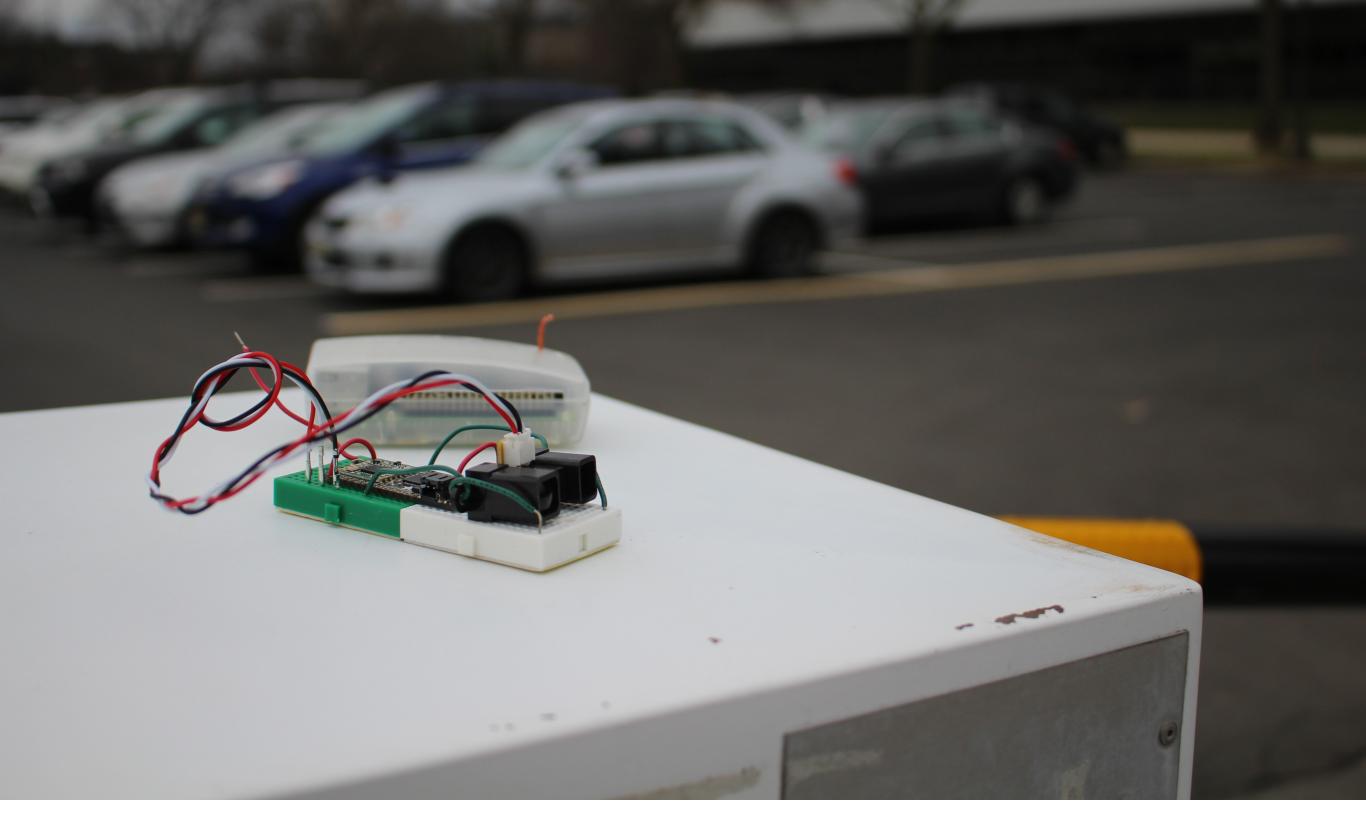
ACTION ITEMS

TASK 1	DESIGN ENCLOSURE
TASK 2	DEVELOP & MODEL DETECTION ALGORITHM
TASK 3	OUTDOOR NODE POWER SYSTEM
TASK 4	WIRELESS NETWORK
TASK 5	FRONTEND SERVER DEVELOPMENT
TASK 6	DETECTION VALIDATION
TASK 7	POWER SYSTEM VALIDATION
TASK 8	WIRELESS VALIDATION
TASK 9	FRONTEND UPTIME & STRESS VERIFICATION

PROGRESS

Semester Timeline

					Period Highlight:	4 Plar	Duration	Actual Start	% Complete	Actual (beyond plan)	% Complete (beyond plan)
Tasks	PLAN START	PLAN DURATION	ACTUAL START	ACTUAL DURATION	PERCENT	EA A LA ES	5 6 7 8	D LO EN N LN ED 8 9 10 11 12 13	D LD EJ J LJ	EF F LF EM M LM EA 3 19 20 21 22 23 24 25	A LA EM M LM 26 27 28 29 30
Design Enclosure	1	18	4	1	10%		•		•		
Develop & Model Detection Algorithm	1	26	1	4	10%			•	• •	• •	
Outdoor Node Power System	1	22	1	4	10%		•	•	•		
Wireless Network	1	25	1	4	10%					•	
Frontend Server Development	1	8	1	4	20%		•				
Detection Validation	9	22	0	0	0%				•	•	•
Power System Validation	15	18	0	0	0%					• •	•
Wireless Validation	19	12	0	0	0%					•	•
Frontend Uptime & Stress Verification	9	22	0	0	0%					•	.





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