



# Parking Lot Availability System using a Campus- Wide Wireless Network

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WARREN SETO

ADVISOR: DR. PEARLSTEIN

# ENGINEERS



**Matthew Cook**  
Computer Engineer

Detection



**Nikita Eisenhauer**  
Electrical Engineer

Power System



**Stephanie Fournier**  
Electrical Engineer

Enclosure & Assembly



**Warren Seto**  
Computer Engineer

Base Station &  
Server

# PROBLEM



# PROBLEM



- Lack of coordination to accommodate growth
- Limited Lots, Limited Spots
- Currently, finding lots...
- Leads to Guessing
- Leads to Wandering
- Leads to Frustrated drivers
- ... all before your first class

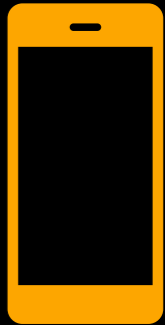
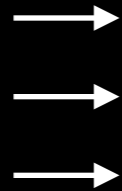
WHAT IF...



# SENIOR PROJECT ARCHITECTURE

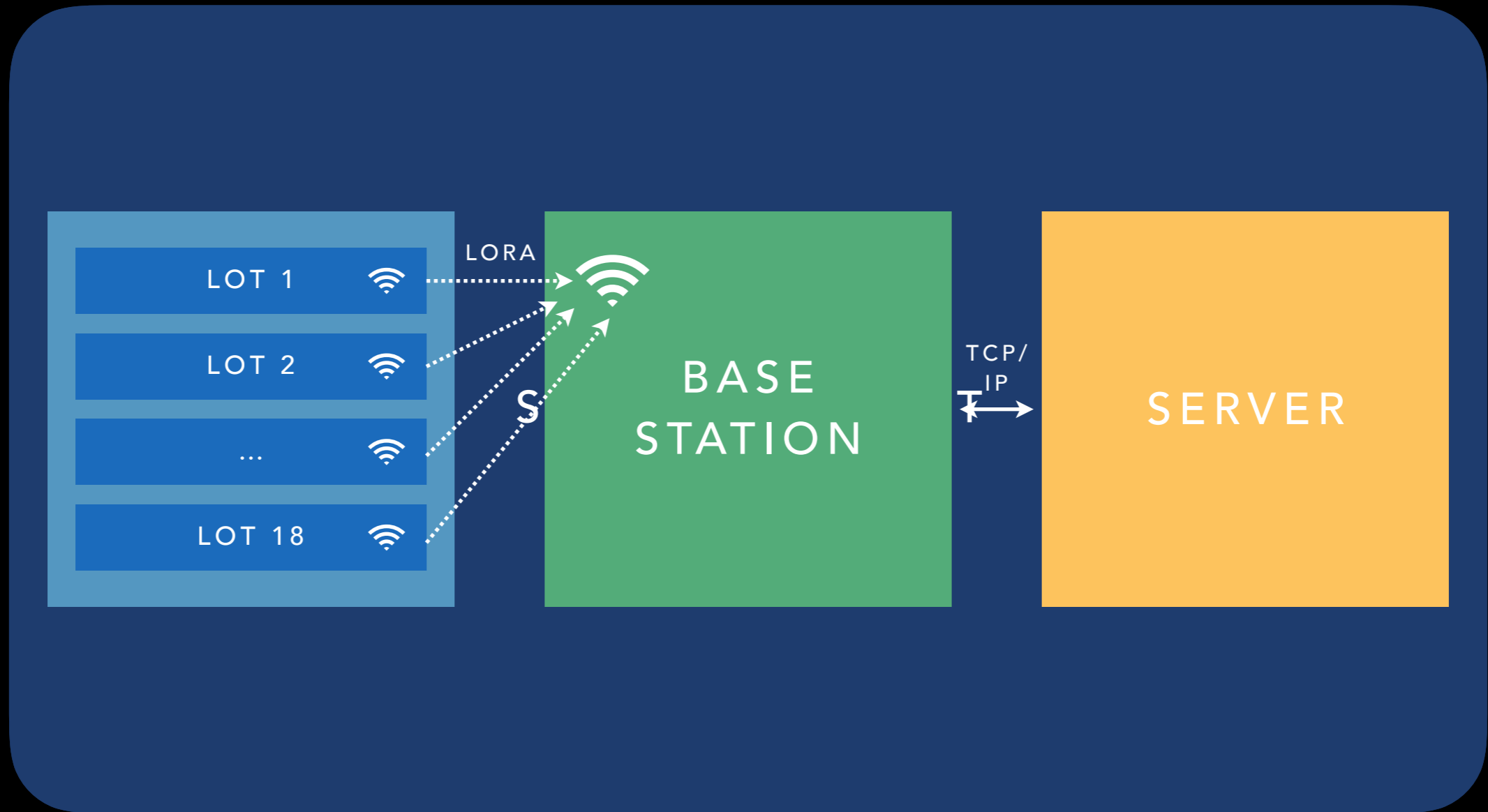


Drivers

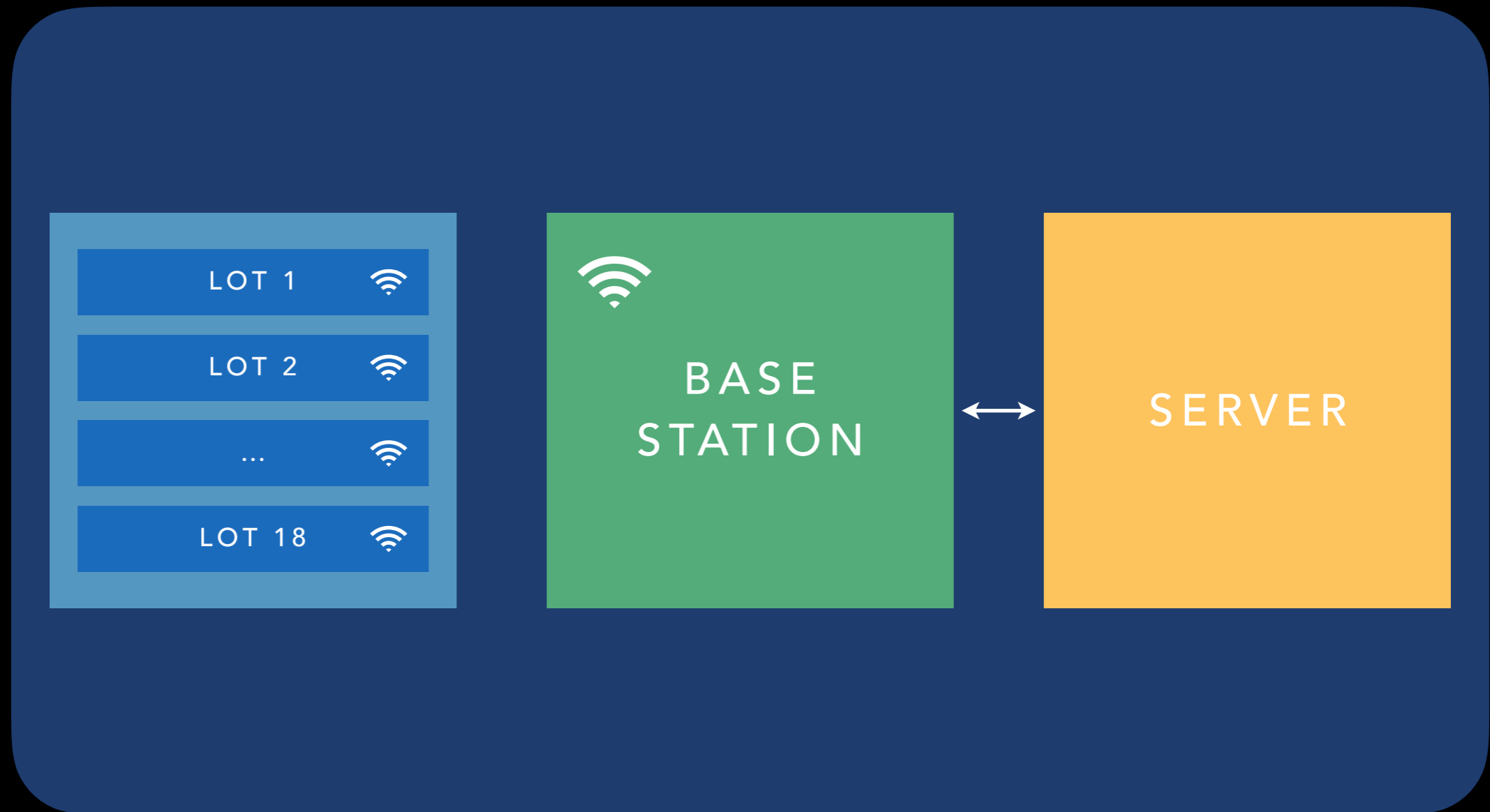


Commuters

# SENIOR PROJECT ARCHITECTURE



# SENIOR PROJECT ARCHITECTURE





# DETECTION UNIT REQUIREMENTS

- Self-powered
- Reliably detect vehicles entering and exiting the parking lot
- Withstand harsh weather conditions and elements



LOT 1

# DETECTION UNIT BREAKDOWN

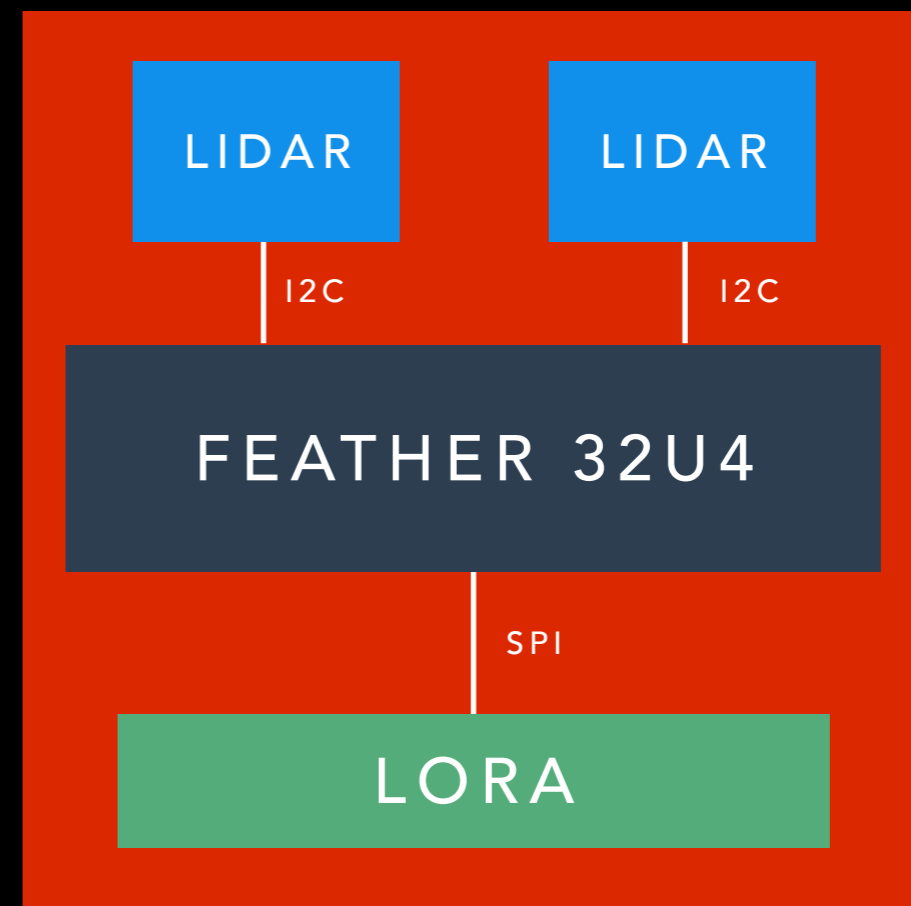
POWER  
SUBSYSTEM

LOT 1

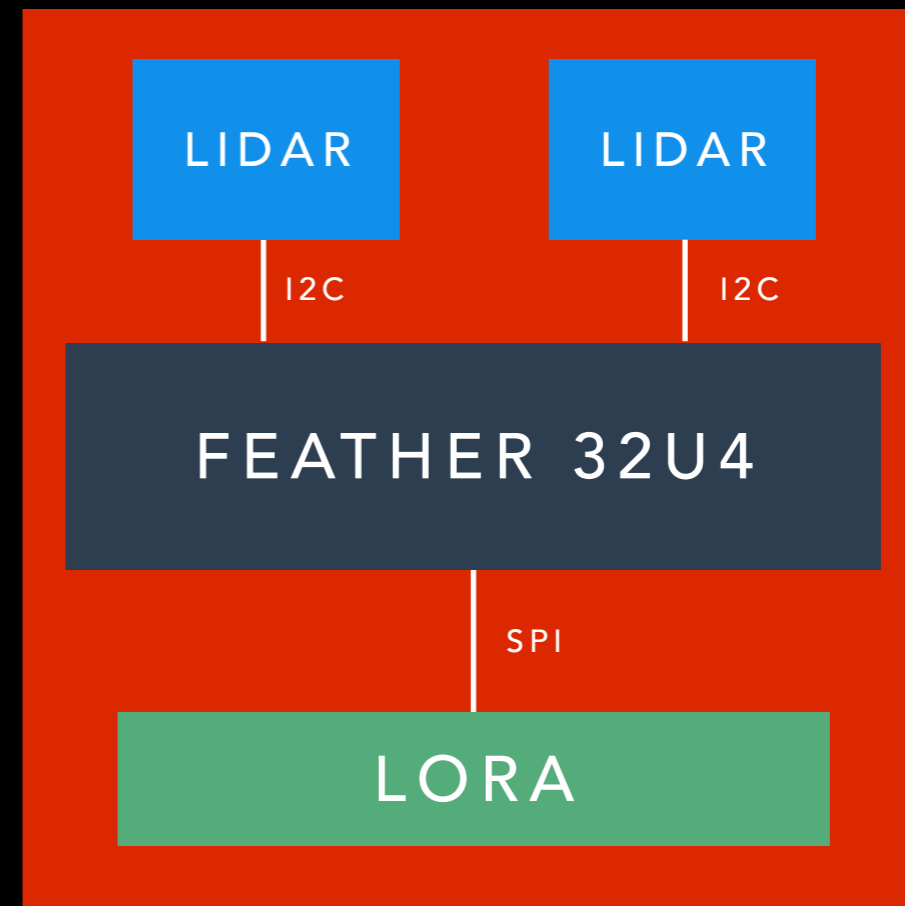
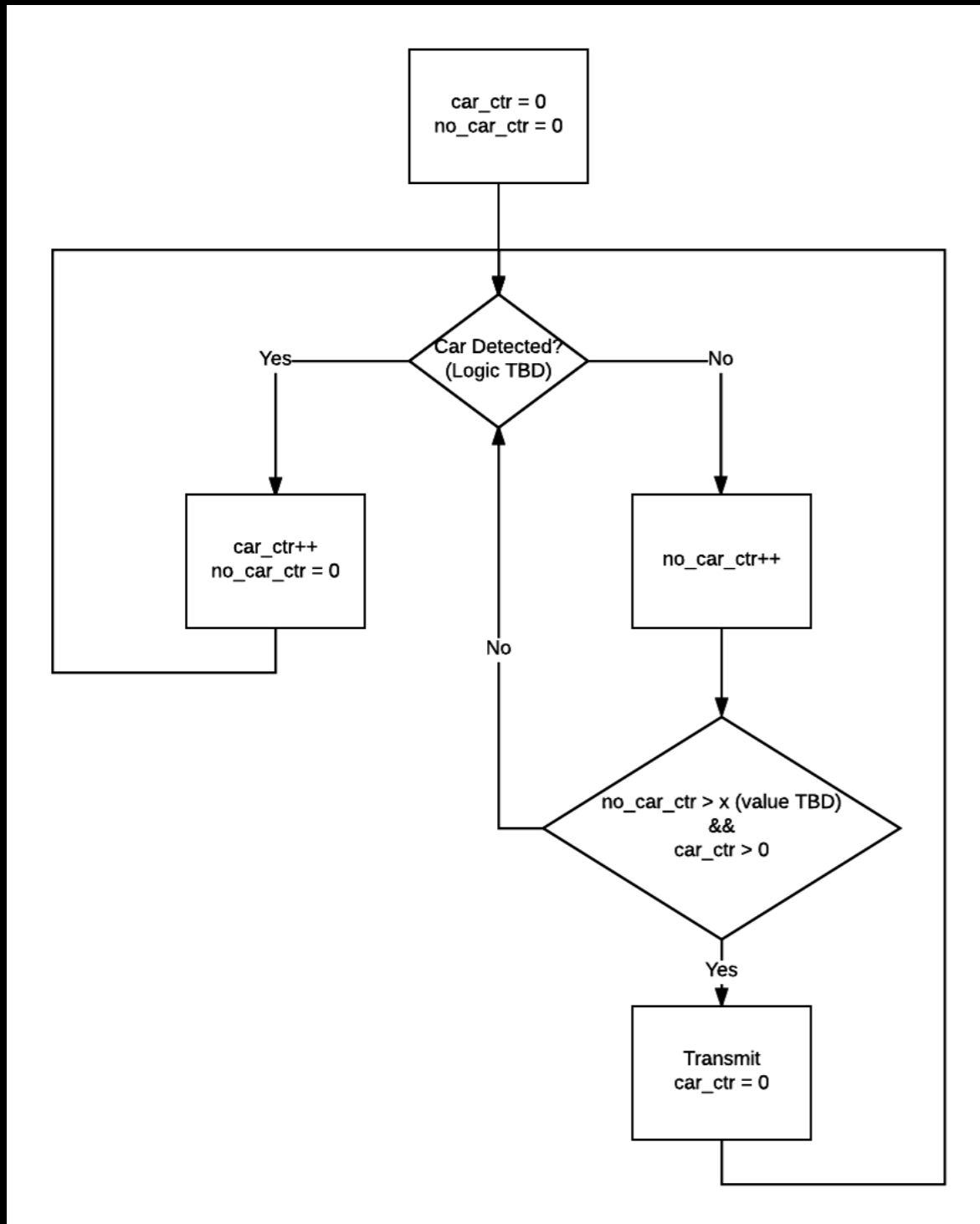
DETECTION UNIT  
CORE

# DETECTION UNIT CORE ARCHITECTURE

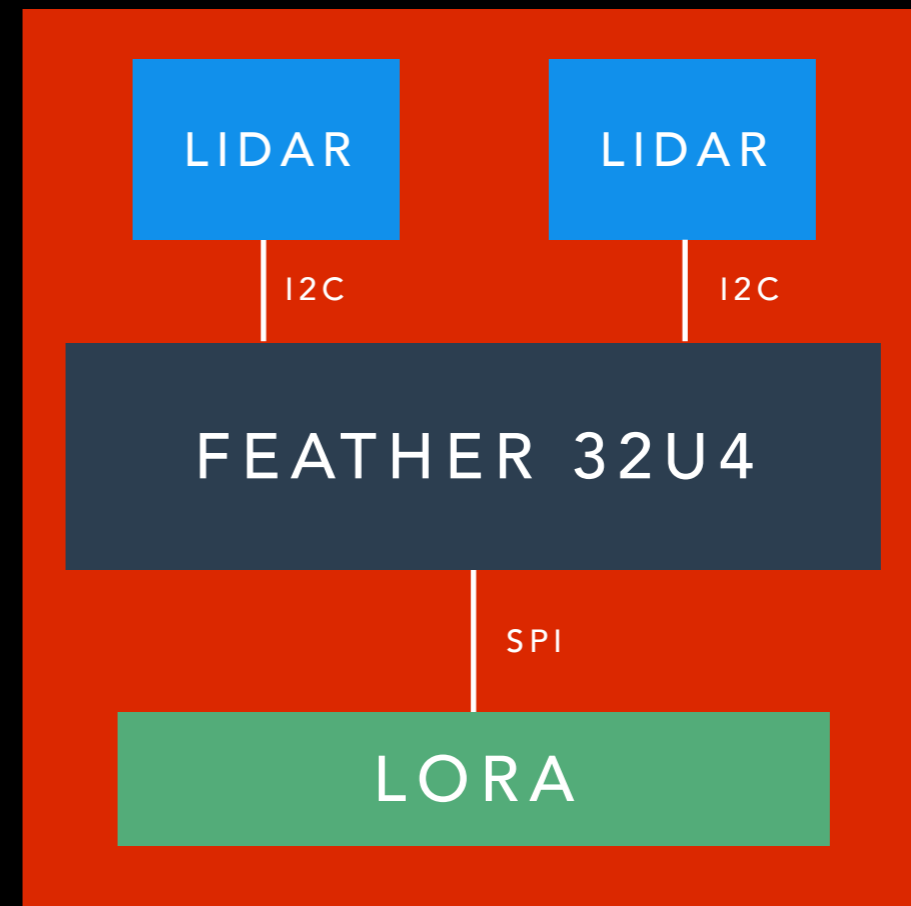
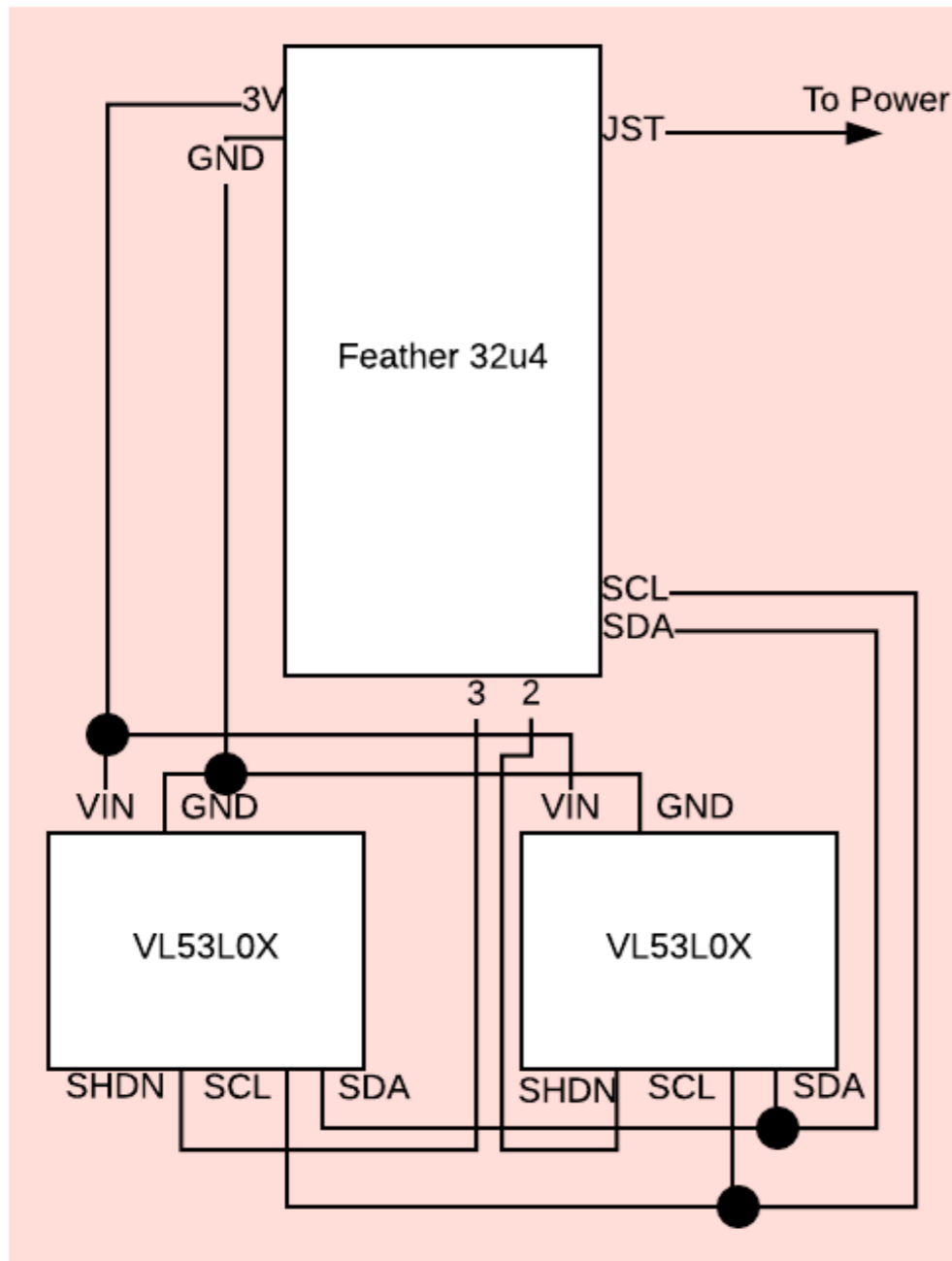
- Arduino Based Microcontroller
- 2 LIDAR Sensors
- Wireless LoRa to communicate with Base Station



# DETECTION UNIT CORE SOFTWARE ARCHITECTURE



# DETECTION UNIT CORE HARDWARE ARCHITECTURE



# DETECTION UNIT CORE PROGRESS

- Completed Tasks
  - Measure data from one LiDar sensor
- Future Tasks
  - Measure data from both LiDar sensors
    - Familiarize with I2C Protocol
    - Create sensor shutdown circuitry



DETECTION UNIT  
CORE

# DETECTION UNIT BREAKDOWN

POWER  
SUBSYSTEM

The diagram consists of a large blue rectangle representing the 'DETECTION UNIT'. Inside this rectangle, on the left side, is a smaller yellow rectangle labeled 'POWER SUBSYSTEM'. On the right side of the blue rectangle is a smaller red rectangle labeled 'DETECTION UNIT CORE'. The two inner rectangles are positioned side-by-side, separated by a gap, and together they occupy most of the width and height of the outer blue rectangle.

DETECTION UNIT  
CORE

POWER  
SUBSYSTEM



# DETECTION UNIT POWER SUBSYSTEM CURRENT ANALYSIS

			Detection Unit Draw	
LoRa (x1)	IR Sensor 1	IR Sensor 2	(V)	(A)
Sleep	Standby	Standby	[3.3V]	314uA
Sleep	Standby/Peak	Standby/Peak	[3.3V]	40.307mA
Sleep	Peak	Peak	[3.3V]	80.3mA
Listening	Standby	Standby	[3.3V]	40.014mA
Listening	Standby/Peak	Standby/Peak	[3.3V]	80.007mA
Listening	Peak	Peak	[3.3V]	120mA
Peak	Standby	Standby	[3.3V]	120.014mA
Peak	Standby/Peak	Standby/Peak	[3.3V]	160.007mA
Peak	Peak	Peak	[3.3V]	200mA

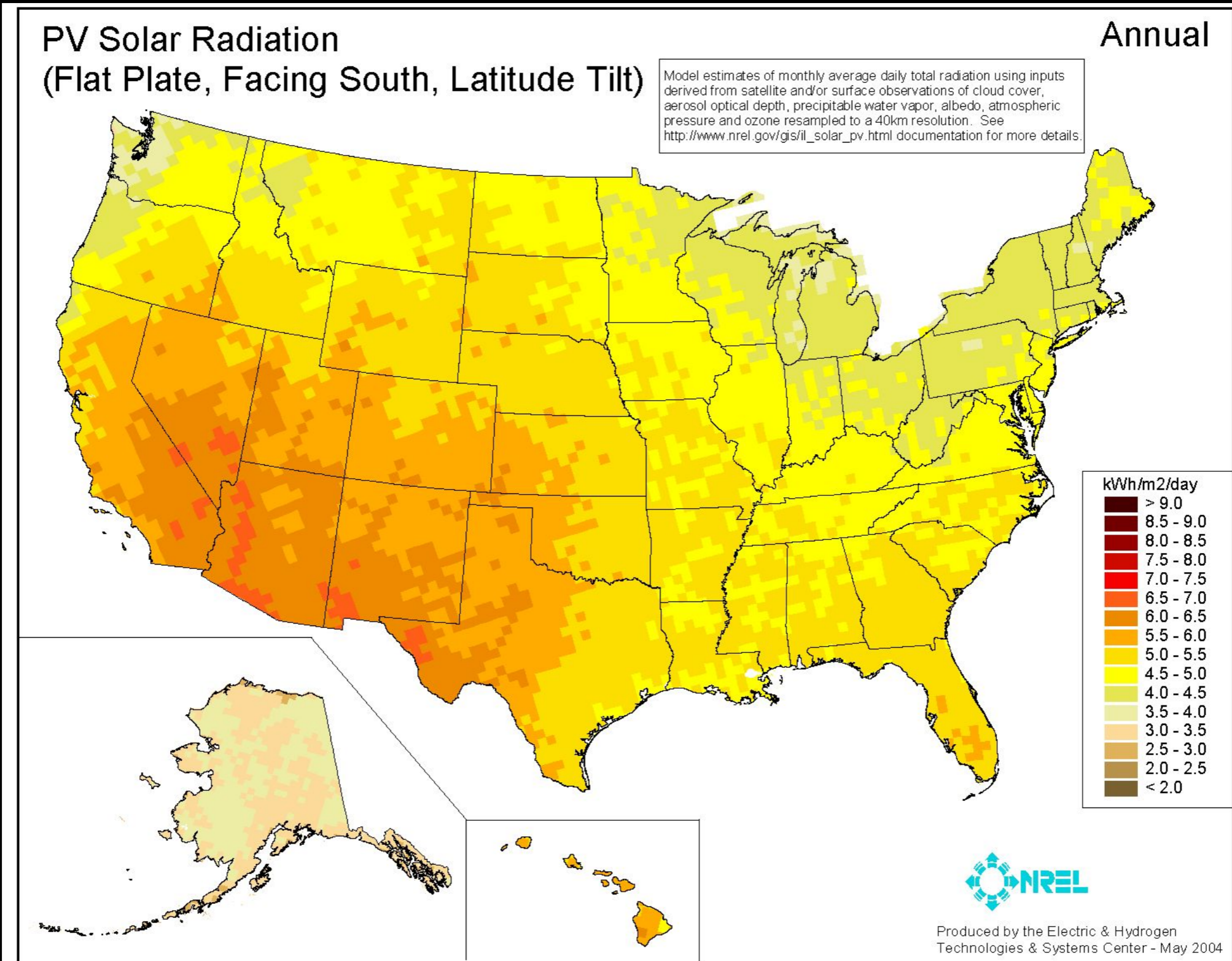
# DETECTION UNIT POWER SUBSYSTEM PARTS

- 5V parallel solar panels w/ Schottky blocking diodes and current sensing resistor
- DC / DC switching converter in step - up configuration
- 4.2 volt regulator w/ 1 amp current limiting in series configuration
- MOSFET battery sub-circuit switch controlled from LoRa module
- High frequency decoupling capacitors for ripple and spike rejection
- Current sensing cutoff at battery terminals



POWER SUBSYSTEM

# DETECTION UNIT POWER SUBSYSTEM GENERAL SOLAR MAP



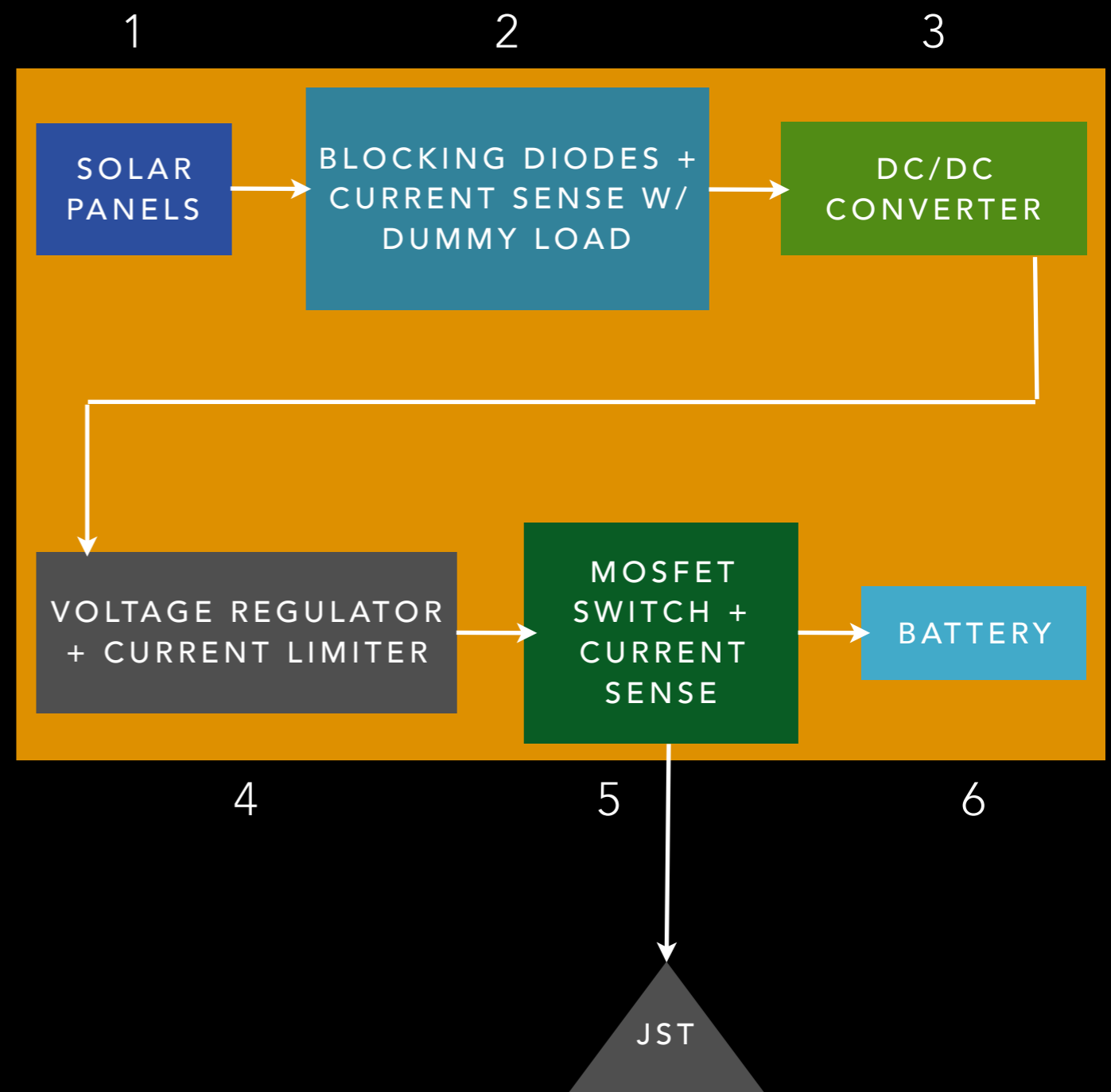
# DETECTION UNIT POWER SUBSYSTEM WIRELESS RANGE MAP



# DETECTION UNIT POWER SUBSYSTEM ARCHITECTURE BREAKDOWN

- **Block Sections**

1. Solar
2. Circuit Protection & Measurement
3. Power Conditioning
4. Power Regulation & Limiting
5. Charging Control & Measurement
6. Battery



# DETECTION UNIT POWER SUBSYSTEM ARCHITECTURE BREAKDOWN

## 1. Solar Panel

- 5 V / 500 mA
- 2.5 W
- Bare bones, no blocking or over-current protection

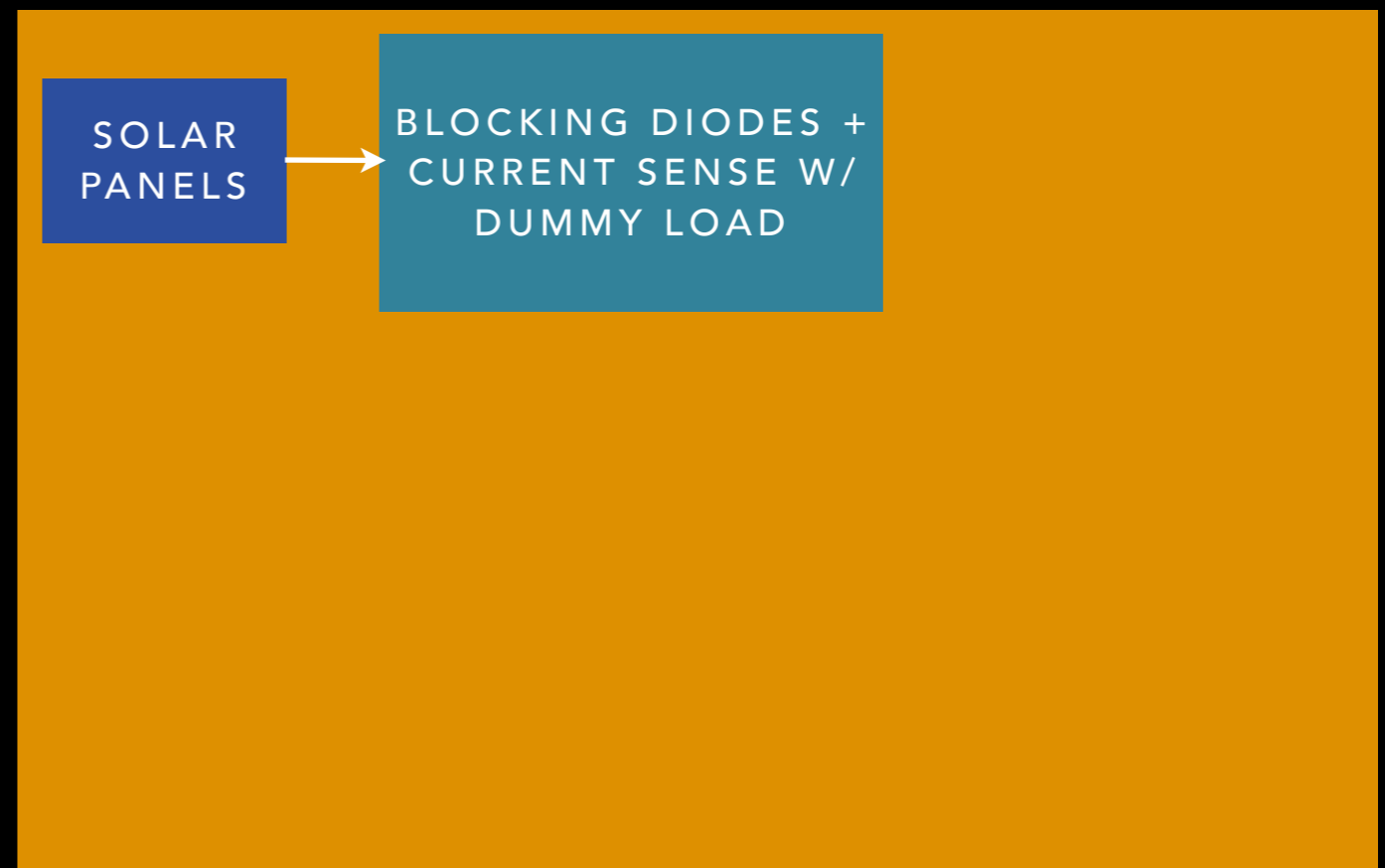


SOLAR  
PANELS

# DETECTION UNIT POWER SUBSYSTEM ARCHITECTURE BREAKDOWN

- **2. Circuit Protection & Measurement**

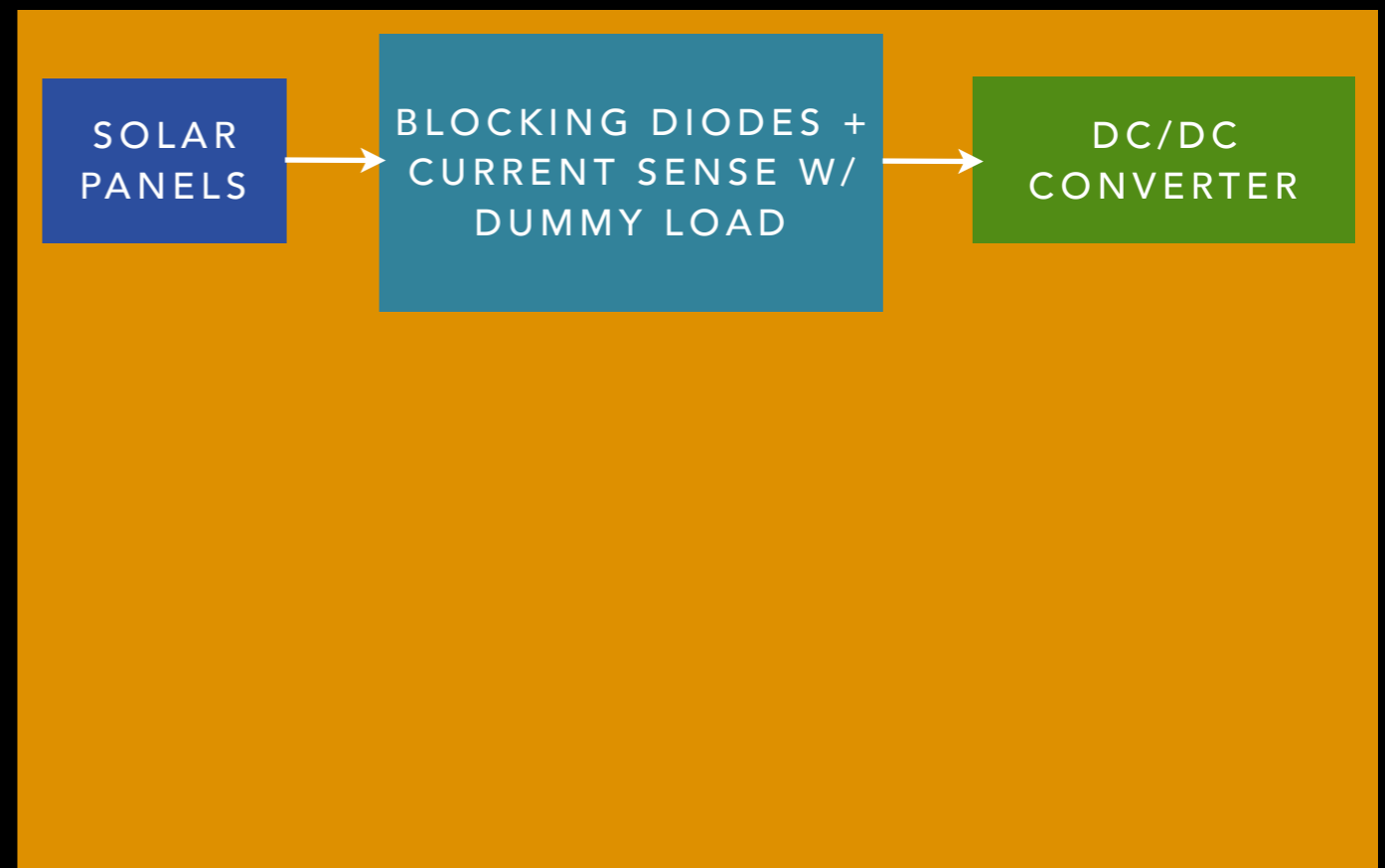
- Schottky blocking diodes
  - Low forward voltage drop
  - Parallel panel feedback blocking
- Low value current sense resistor w/ dummy load switching
  - Determine sunlight conditions and solar panel operation



# DETECTION UNIT POWER SUBSYSTEM ARCHITECTURE BREAKDOWN

## 3. Power Conditioning

- DC/DC switching converter
  - Step up voltage
  - High efficiency / low power loss

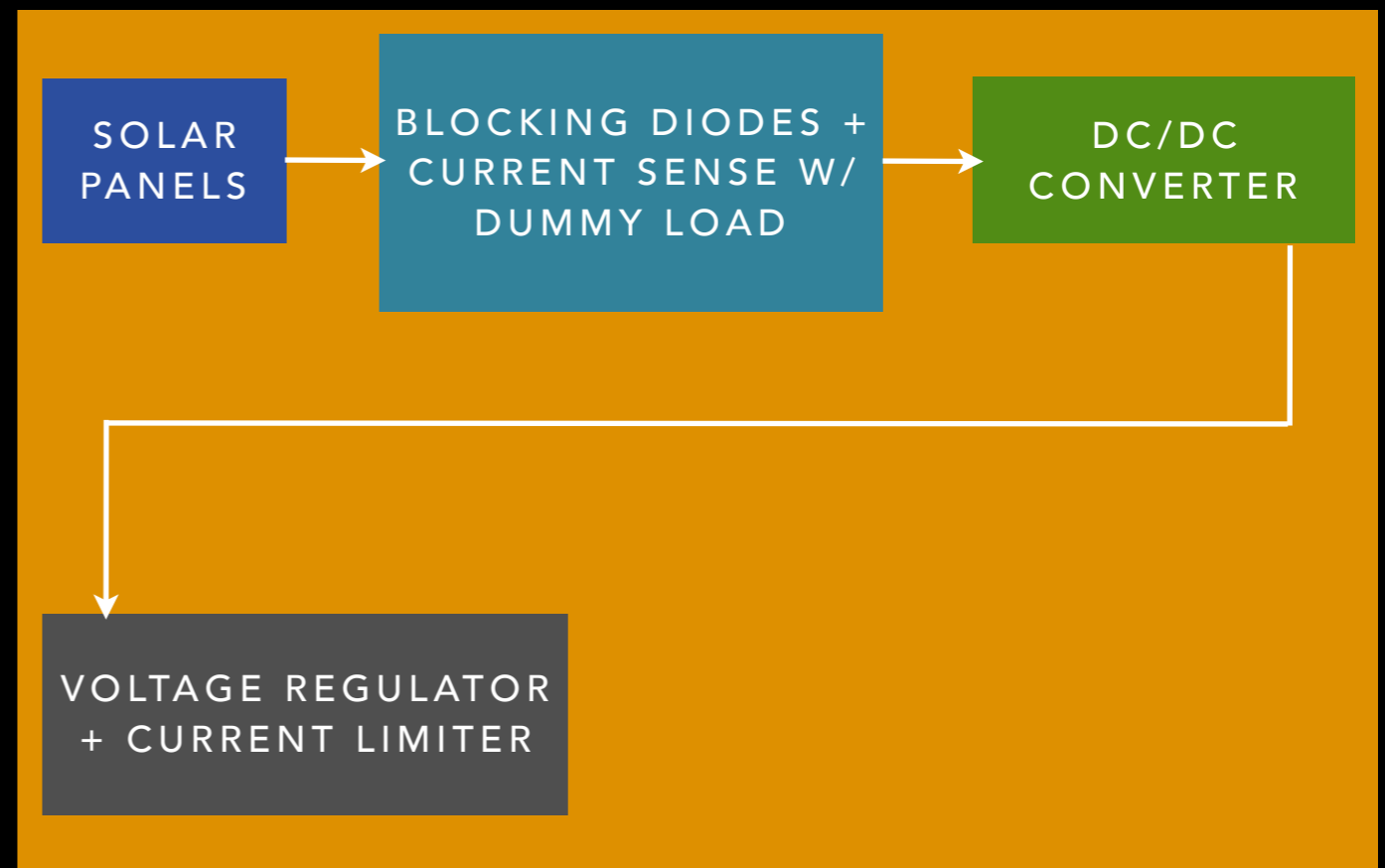




# DETECTION UNIT POWER SUBSYSTEM ARCHITECTURE BREAKDOWN

- **4. Power Regulation & Limiting**

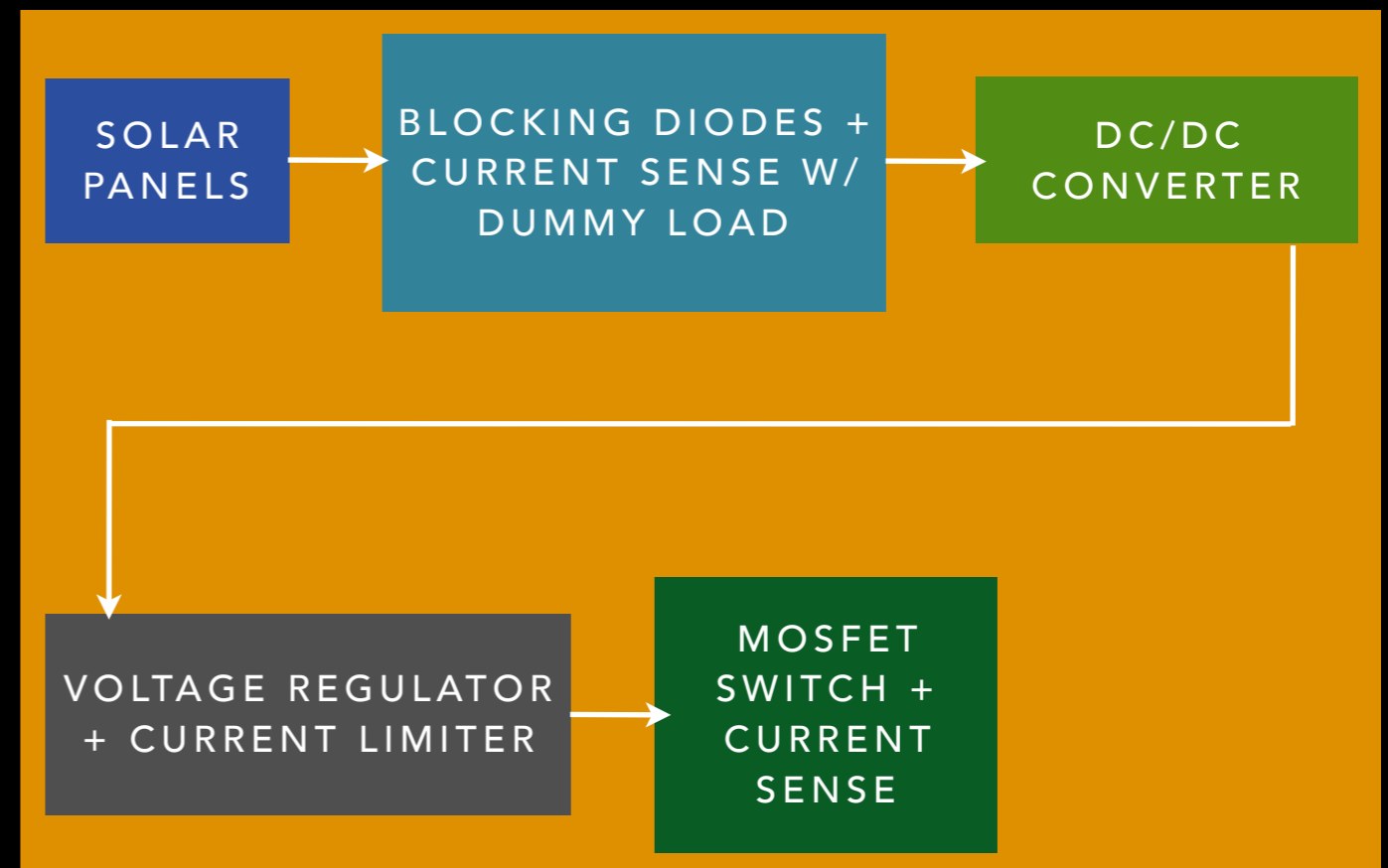
- LM317 linear regulator
  - Variable output voltage
  - Current limiting capabilities



# DETECTION UNIT POWER SUBSYSTEM ARCHITECTURE BREAKDOWN

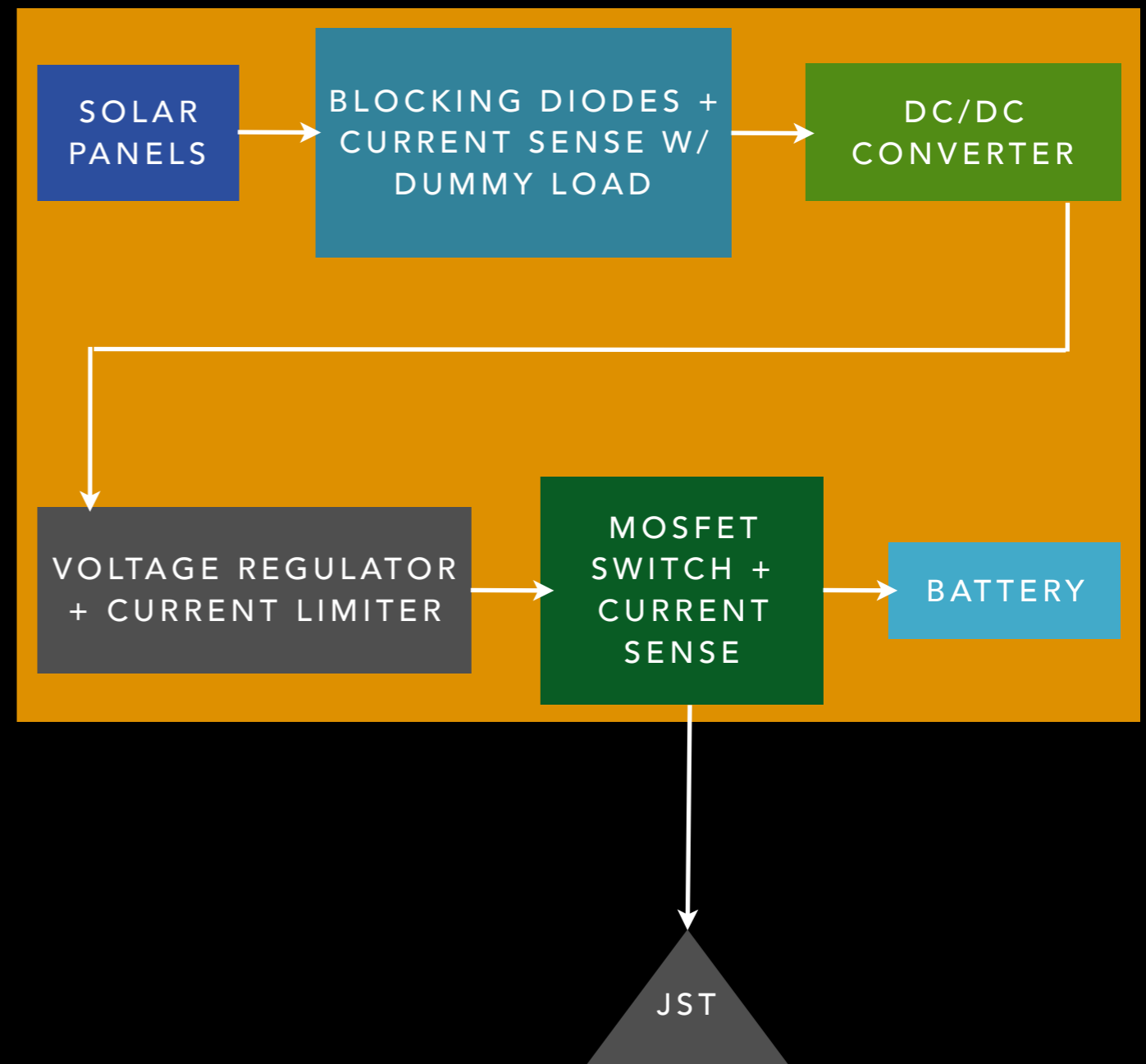
- **5. Charging Control & Measurement**

- MOSFET switch
  - Control charging / discharging durations of battery
- Low value current sense resistor
  - Determine battery capacity

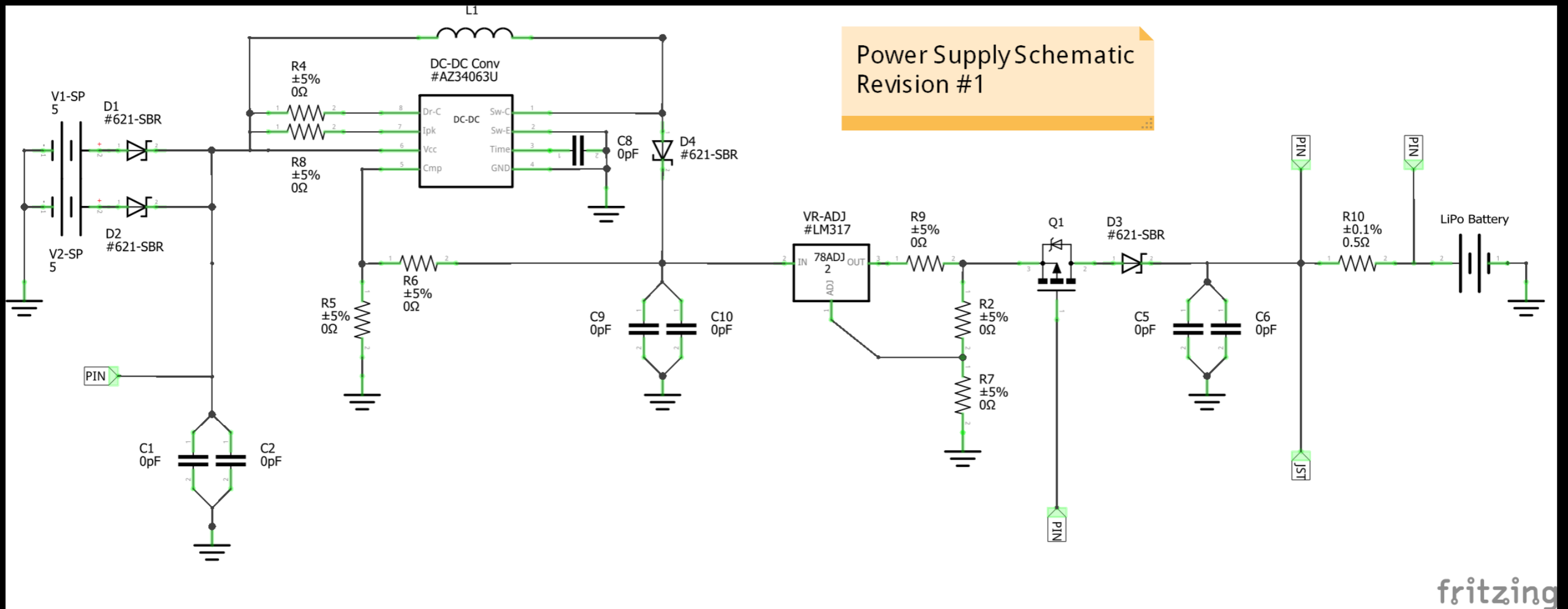


# DETECTION UNIT POWER SUBSYSTEM ARCHITECTURE BREAKDOWN

- **6. Battery**
  - 4.2 / 3.7 V
  - 4000 mAh
  - Battery cutoff circuitry



# DETECTION UNIT POWER SUBSYSTEM SCHEMATIC



# DETECTION UNIT POWER SUBSYSTEM PROGRESS

## Completed

- Solar and battery system background research
- Circuit current draws and demands
- Initial necessary constituent components
- Initial schematic design

## In Progress

- Final component selection
- Battery performance and weekly traffic prediction
- Schematic revision
- Circuit simulation (individual components and total circuit)
- Solar panel Voc and Isc testing, plus shade performance and load testing



POWER SUBSYSTEM

# DETECTION UNIT BREAKDOWN

The diagram consists of a large blue rectangle representing the Detection Unit. Inside this rectangle are two smaller squares: a yellow one on the left and a red one on the right. The yellow square is labeled 'POWER SUBSYSTEM' and the red square is labeled 'DETECTION UNIT CORE'.

POWER  
SUBSYSTEM

DETECTION UNIT  
CORE

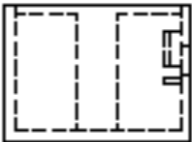
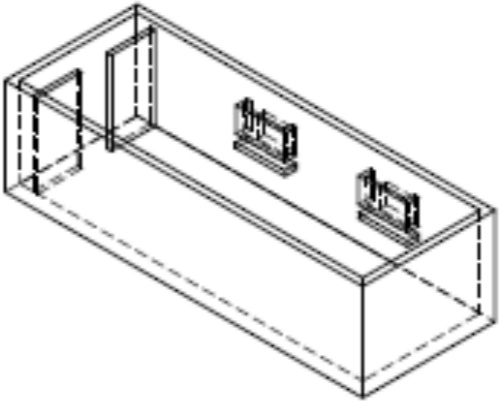
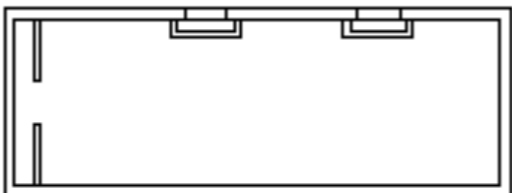
# DETECTION UNIT ENCLOSURE DESIGN

- Object of enclosure is to ensure security to parts and to keep them in place.
- Must conserve space to conserve material.
- For detection unit, sensors are mounted on the wall of the enclosure with an opening.



ENCLOSURE

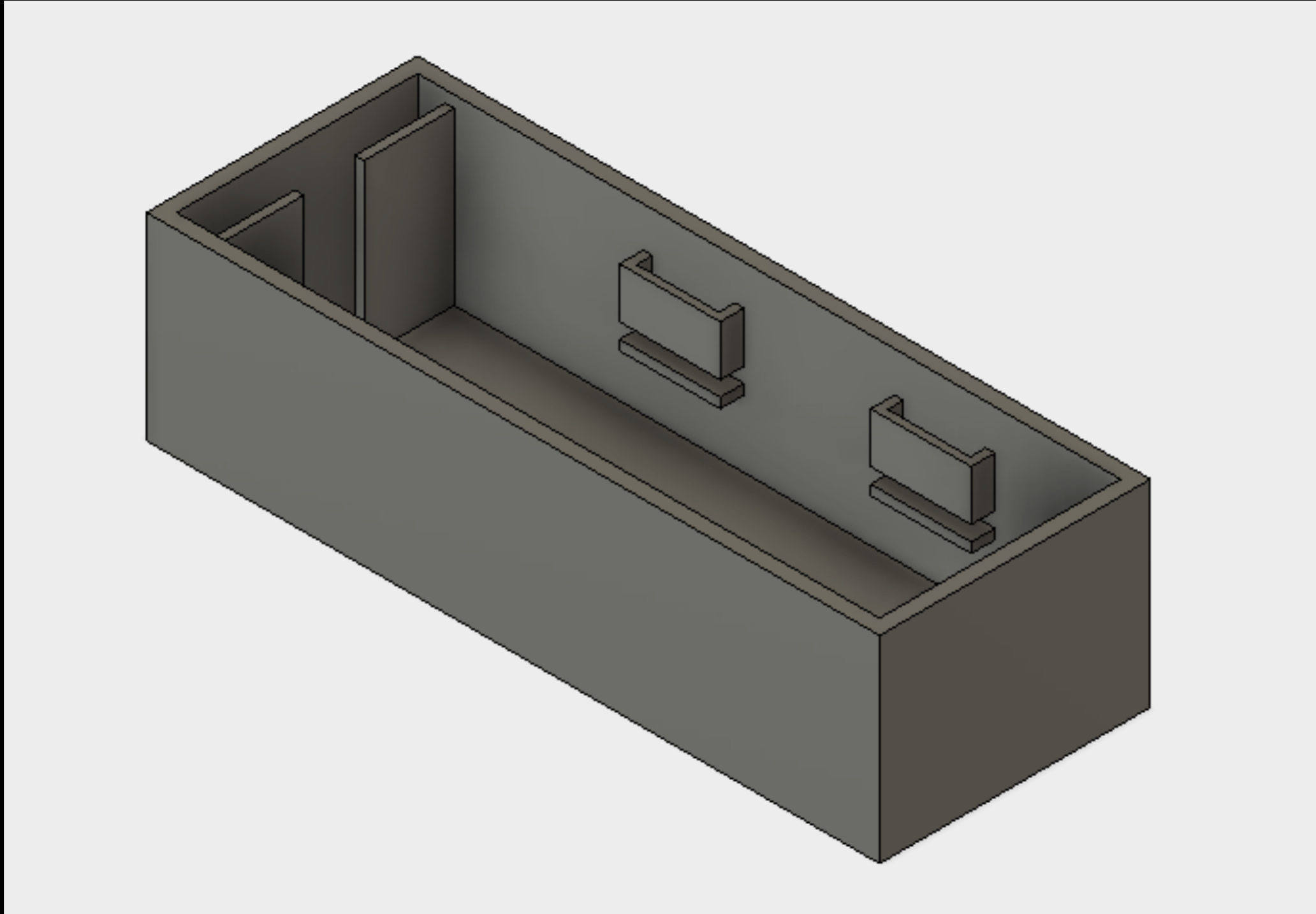
# DETECTION UNIT ENCLOSURE DRAWING



		PROJECT Senior Project		
		TITLE N1 P1 v5		
APPROVED	SIZE	CODE	DWG NO	REV
CHECKED	B			
DRAWN	Stephanie Fourie@/1/20	SCALE 1: 2	WEIGHT	SHEET 1/1



# DETECTION UNIT ENCLOSURE RENDER



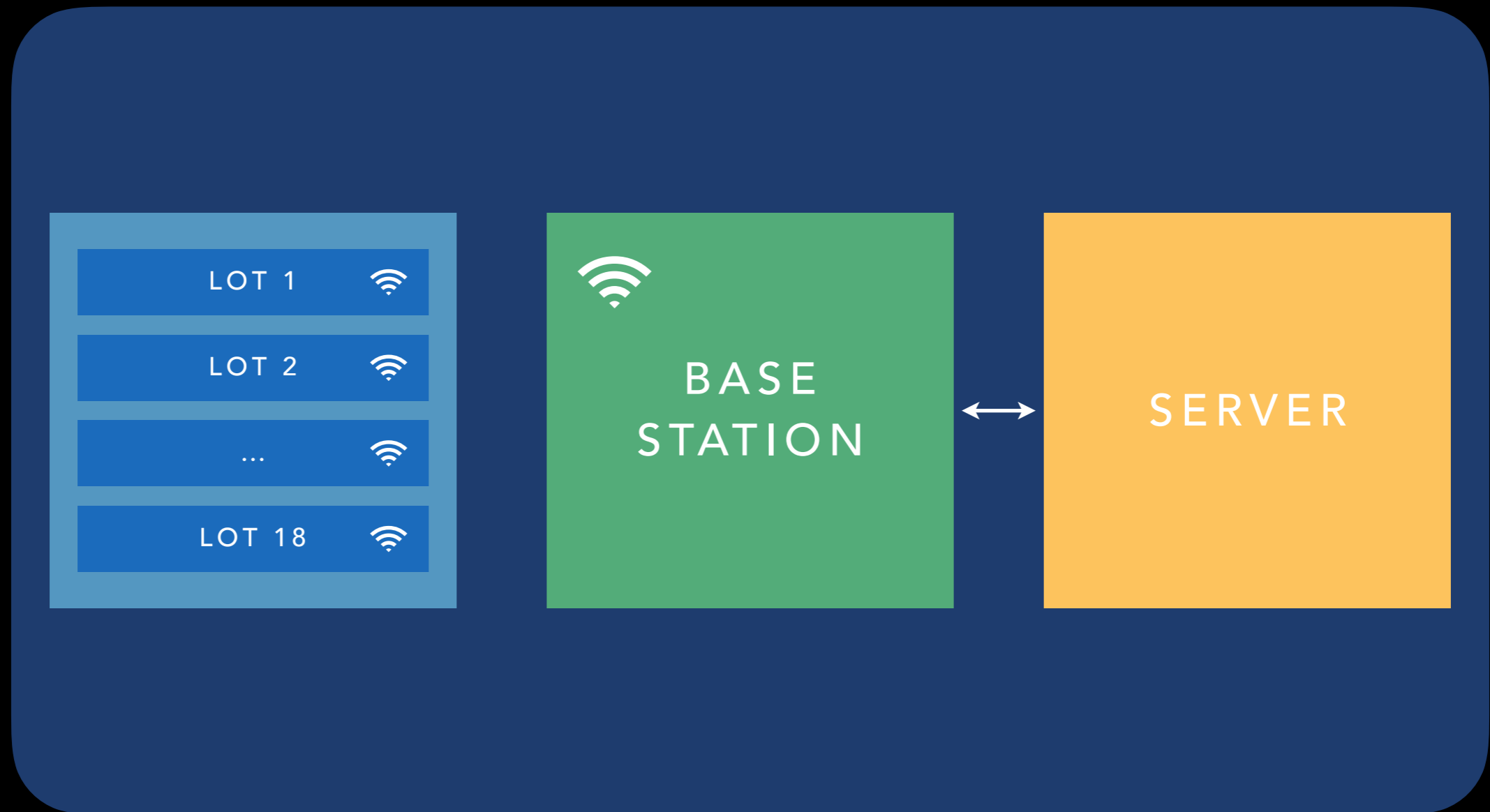
# DETECTION UNIT BREAKDOWN

The diagram illustrates the components of a Detection Unit. It features a large blue rectangular background. Inside this background, there are two smaller, side-by-side rectangular boxes. The box on the left is yellow and contains the text 'POWER SUBSYSTEM'. The box on the right is red and contains the text 'DETECTION UNIT CORE'.

POWER  
SUBSYSTEM

DETECTION UNIT  
CORE

# SENIOR PROJECT ARCHITECTURE



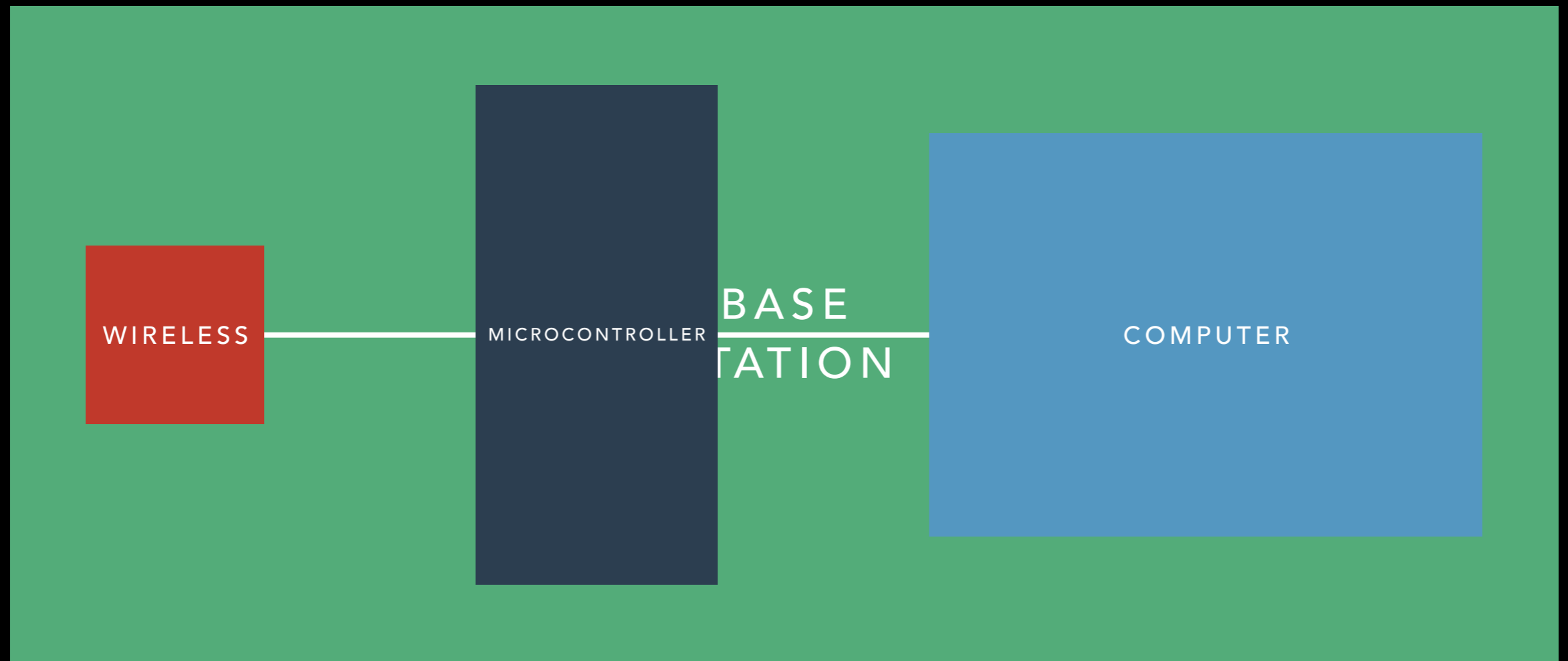
# BASE STATION REQUIREMENTS

- Accept incoming payloads from multiple detection units
- Reliable connection to AWS
- Can be updated with new software



BASE  
STATION

# BASE STATION REQUIREMENTS ARCHITECTURE



WIRELESS

# COMPARISON

	WIFI	BLUETOOTH	ZIGBEE	CELLULAR	LORA
CURRENT DRAW TX/RX (MILLIAMPS)	172/70	11/12.5	33/28	344/227	100/16
LINE OF SIGHT RANGE (METERS)	100	100	120	CELLULAR NETWORK COVERAGE	2000
COST (USD \$)	25	20	23	40	20

All values are derived from products sold by Adafruit Inc. as of October 2017

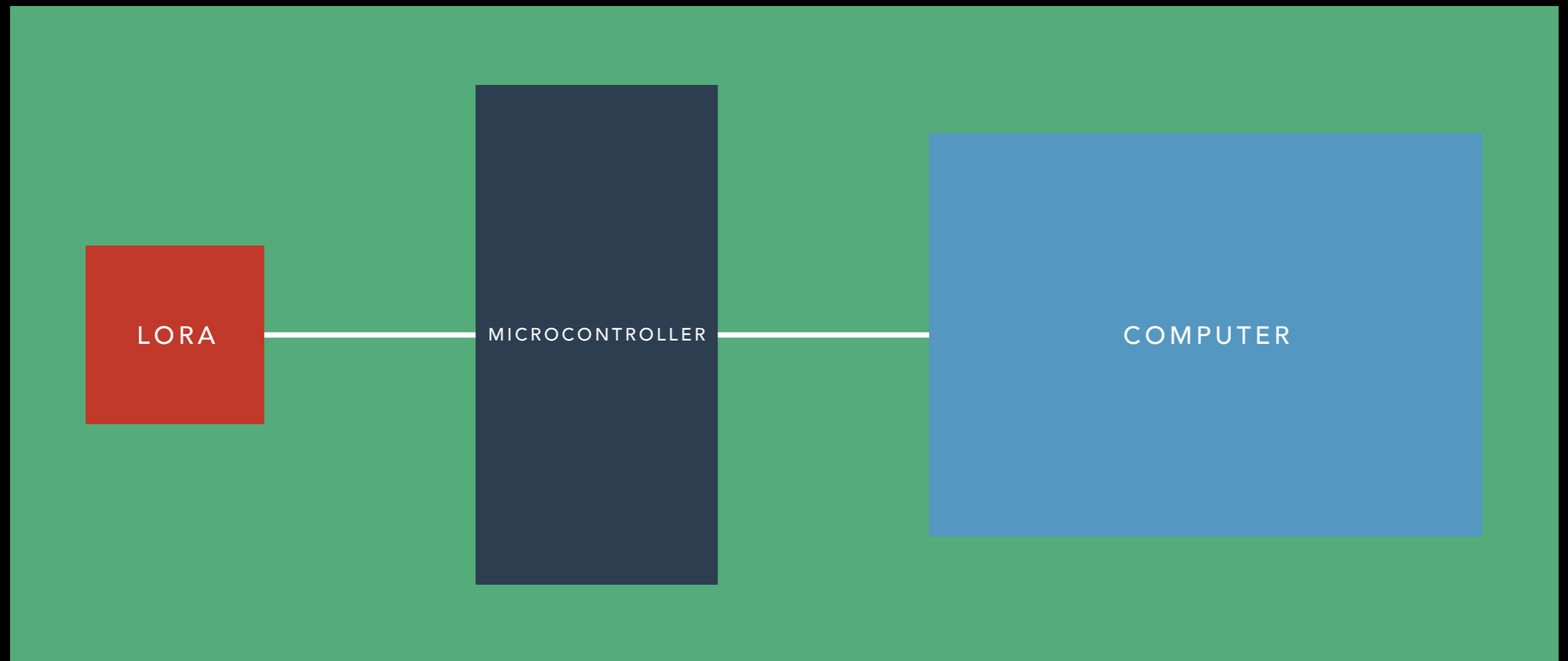
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# BASE STATION REQUIREMENTS ARCHITECTURE





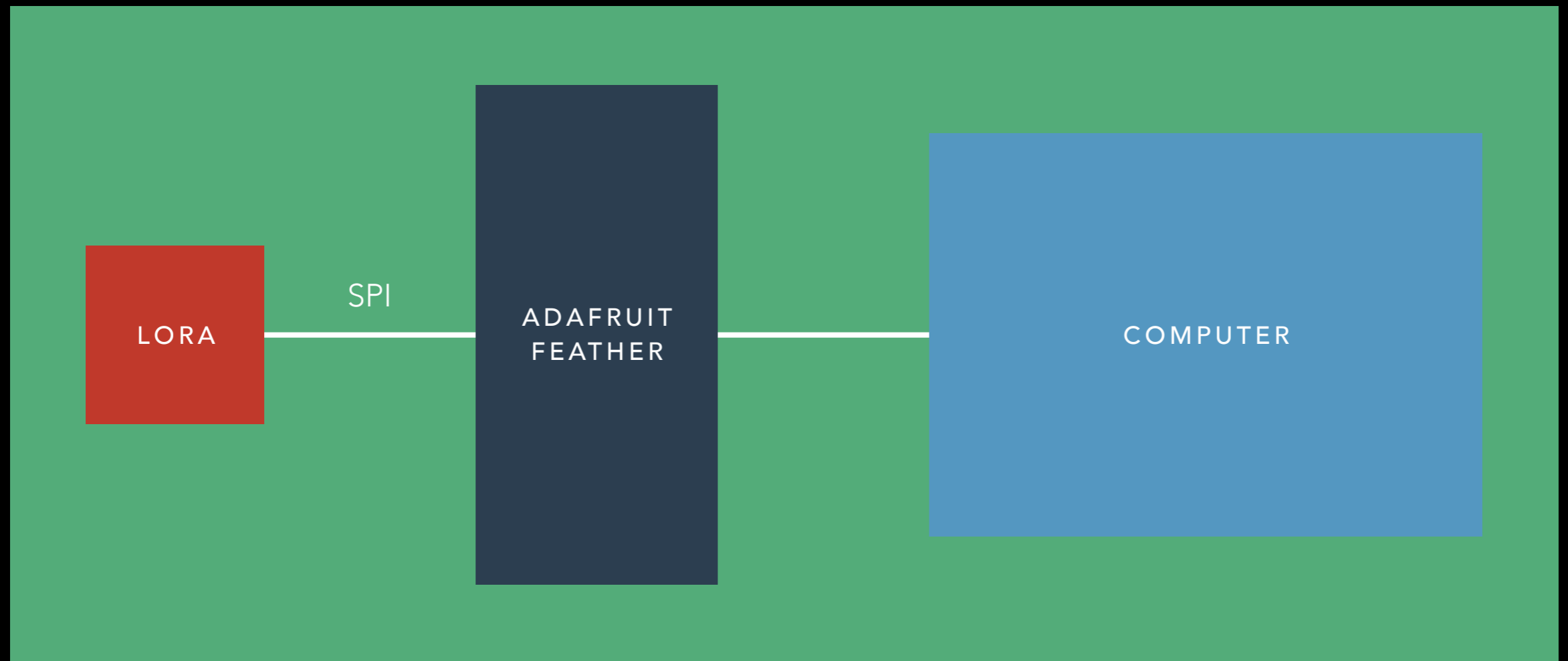
## COMPARISON

	ARDUINO UNO	ARDUINO MKR WAN	PARTICLE PHOTON	CYPRESS PSOC	ADAFRUIT FEATHER
CLOCK SPEED (MHZ) & (BIT)	16 (8-BIT)	32 (32-BIT)	120 (32-BIT)	48 (32-BIT)	48 (32-BIT)
FLASH MEMORY (KB)	32	256	1024	256	256
RAM (KB)	2	32	128	1024	32
COST (USD \$)	25	35	20	45	35
ONBOARD RADIO OPTIONS	-	LORA	WIFI / CELLULAR	WIFI / BLUETOOTH	WIFI / BLUETOOTH / LORA

## COMPARISON

	ARDUINO UNO	ARDUINO MKR WAN	PARTICLE PHOTON	CYPRESS PSOC	ADAFRUIT FEATHER
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# BASE STATION REQUIREMENTS ARCHITECTURE



## COMPUTER

## COMPARISON

	DELL GX520	ADAFRUIT FEATHER M0	BEAGLEBONE BLACK	C.H.I.P	RASPBERRY PI 3
CLOCK SPEED (GHZ)	2.8 (DUAL CORE)	48 (SINGLE CORE)	1 (SINGLE CORE)	1 (SINGLE CORE)	1.2 (QUAD CORE)
DISK (GB)	400	0.000256	4	4	4
RAM (GB)	1	0.000032	0.5	0.5	1
WEIGHT (GRAMS)	8700	3.08	40.82	1.81	136
COST (USD \$)	100	40	55	10	35
BUILT IN CONNECTIVITY	ETHERNET	ETHERNET	WIFI + BLUETOOTH	WIFI + BLUETOOTH	ETHERNET + WIFI + BLUETOOTH

All values are derived from products sold by Adafruit Inc. and Amazon as of October 2017

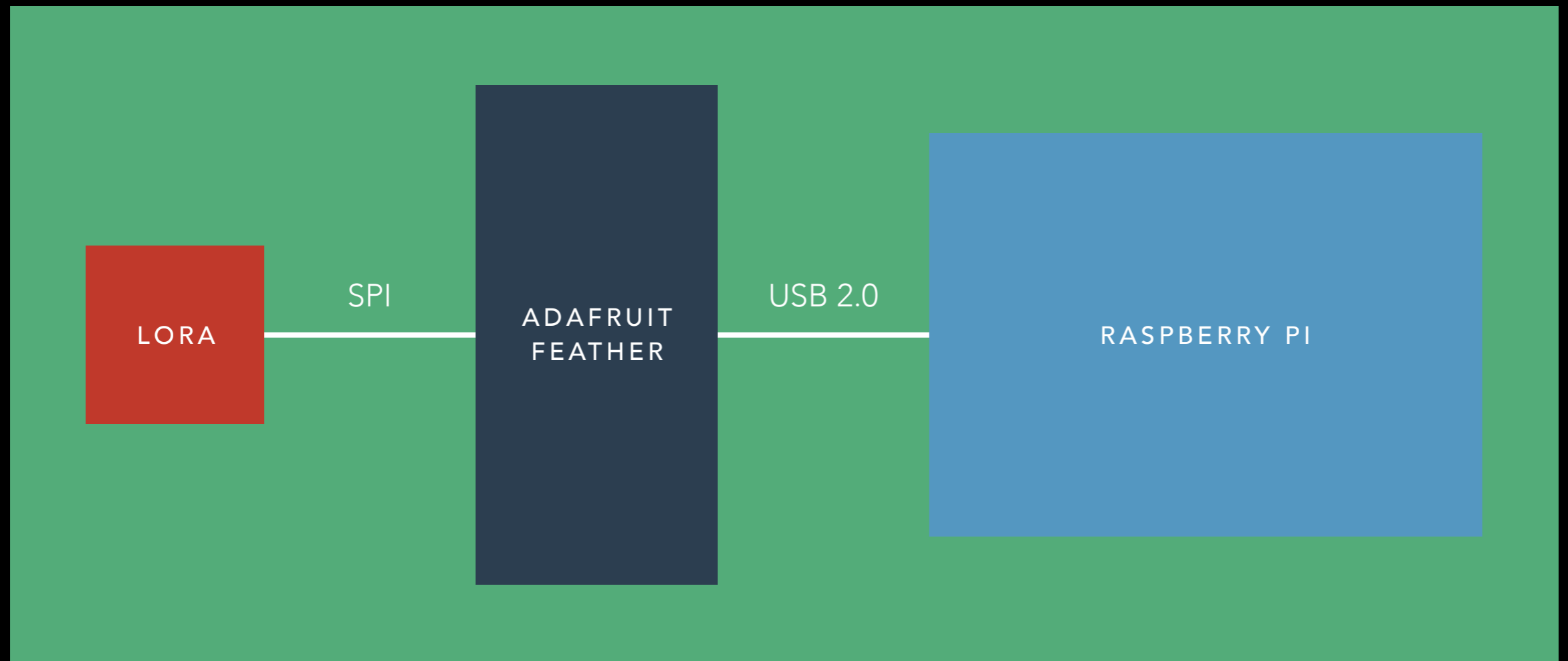
COMPUTER

## COMPARISON

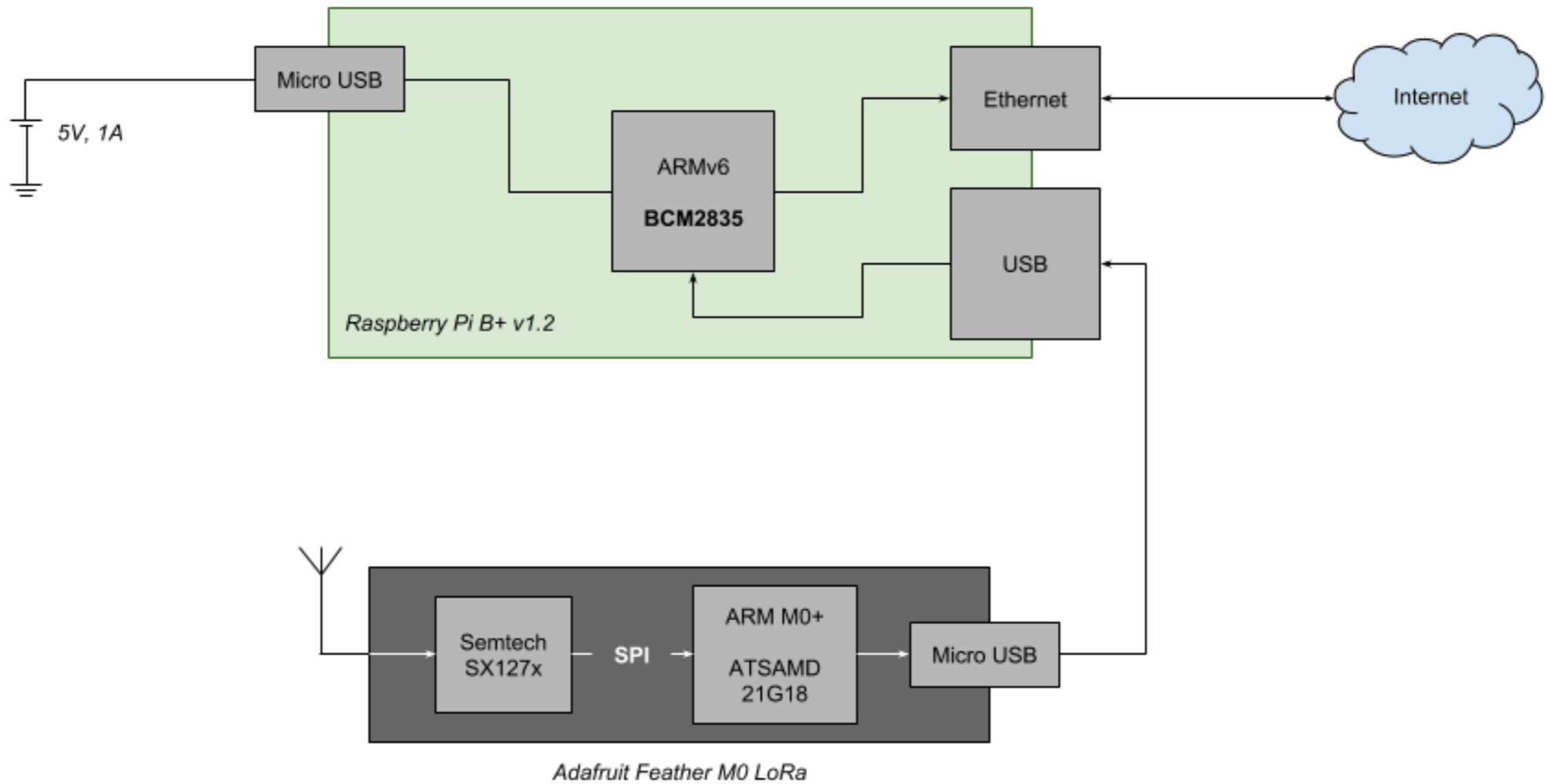
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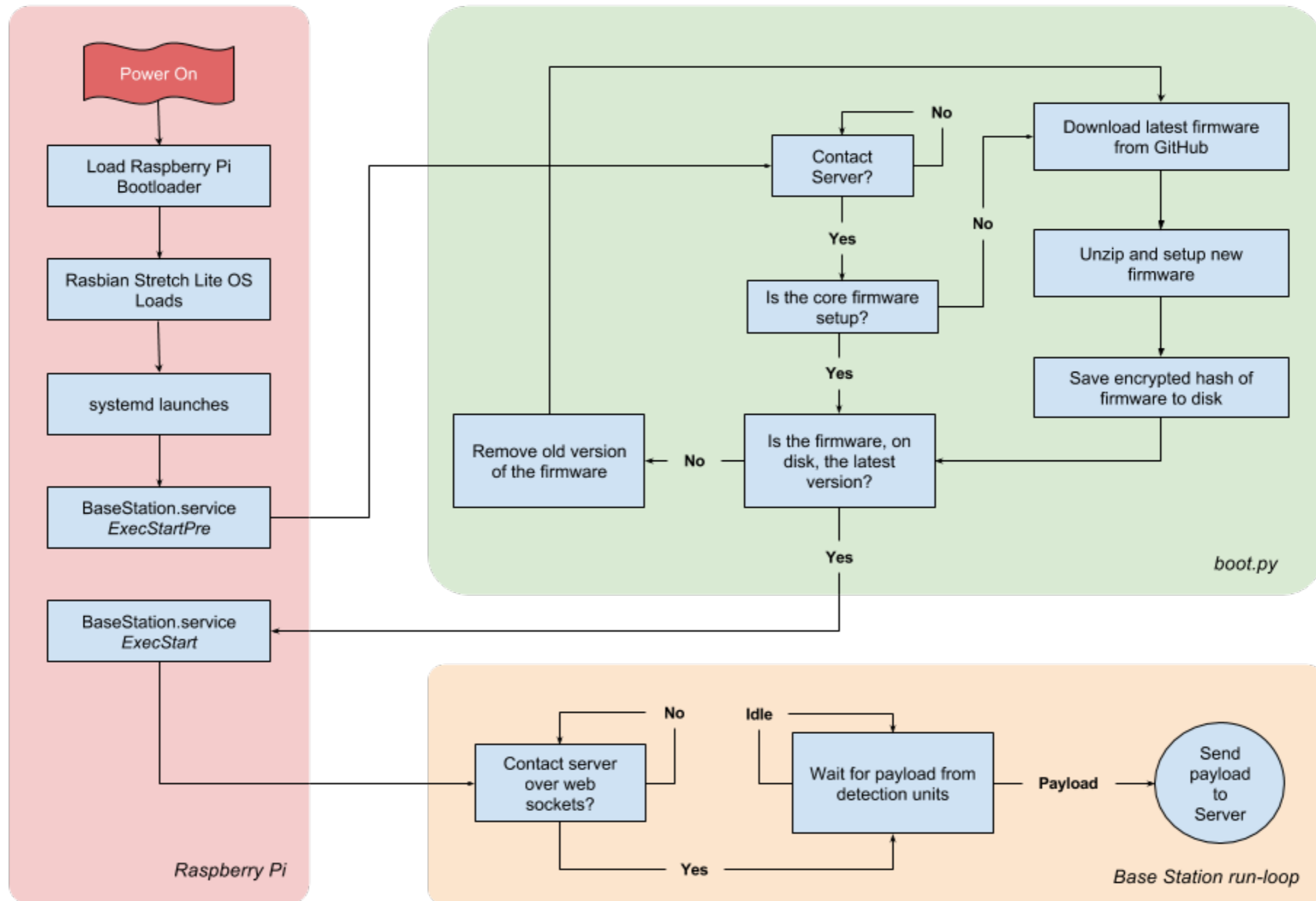
# BASE STATION REQUIREMENTS ARCHITECTURE



# BASE STATION HARDWARE ARCHITECTURE



# BASE STATION SOFTWARE ARCHITECTURE

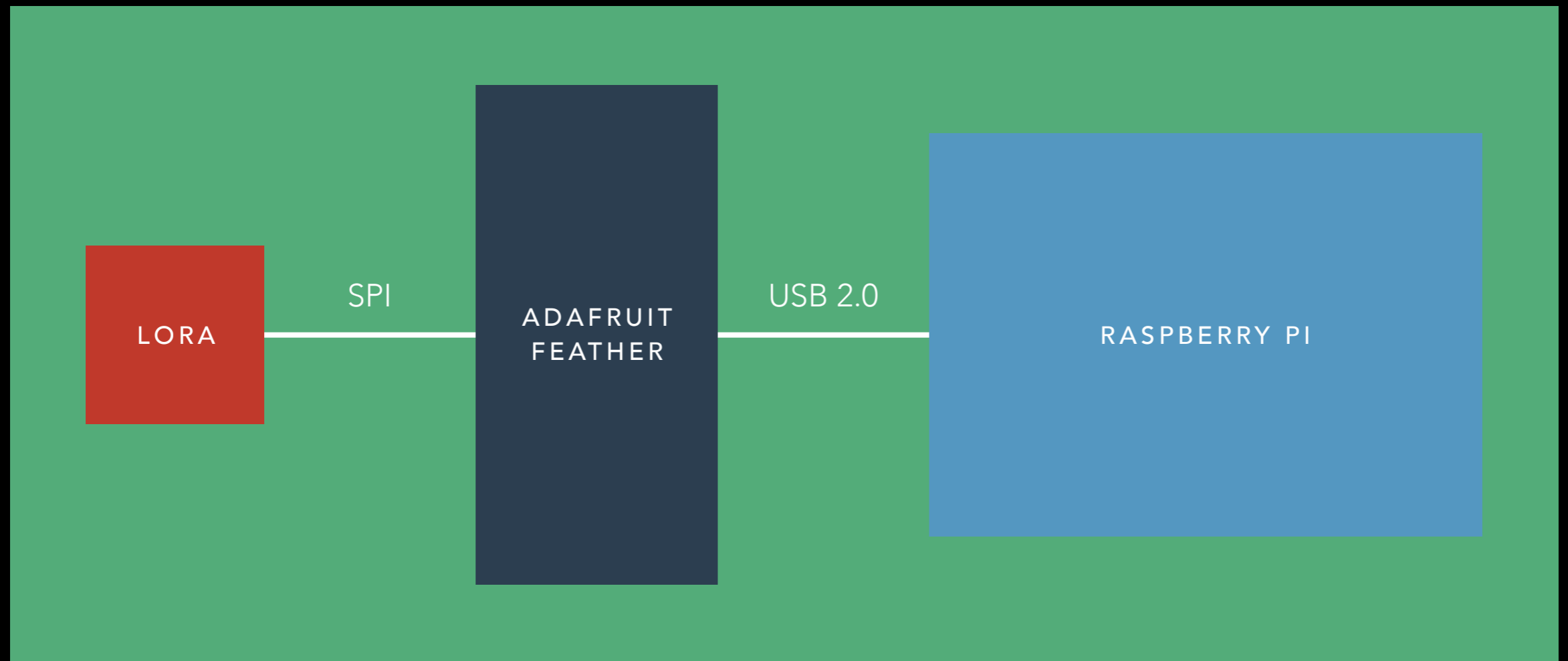




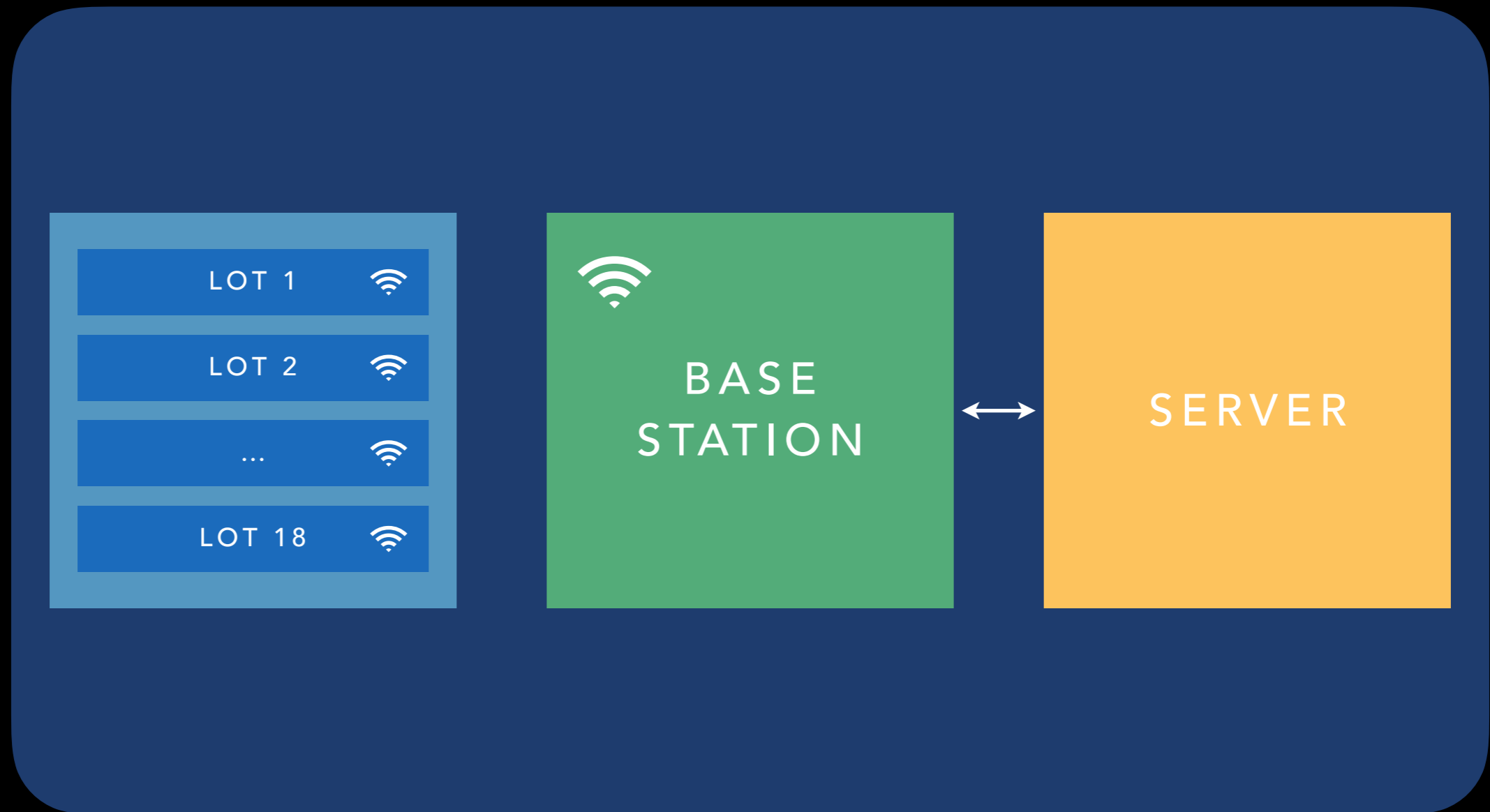
# UPCOMING BASE STATION DEVELOPMENT TASKS

1	SPEED UP BASE STATION BOOT UP
2	STABILIZE V1.0 FIRMWARE FOR RASPBERRY PI
3	REFACTOR V1.0 FIRMWARE FOR ARDUINO WIRELESS RECEIVER
4	IMPLEMENT PROTOCOL FOR BASE STATION TO DETECTION UNIT COMMUNICATION

# BASE STATION REQUIREMENTS ARCHITECTURE

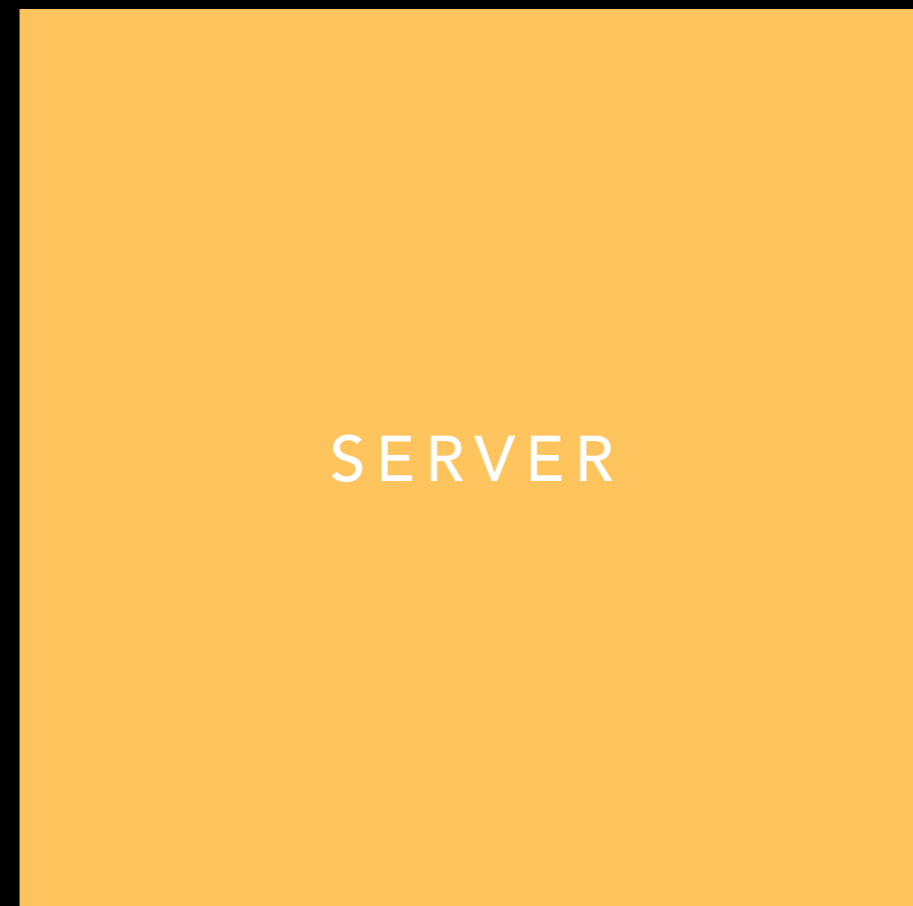


# SENIOR PROJECT ARCHITECTURE

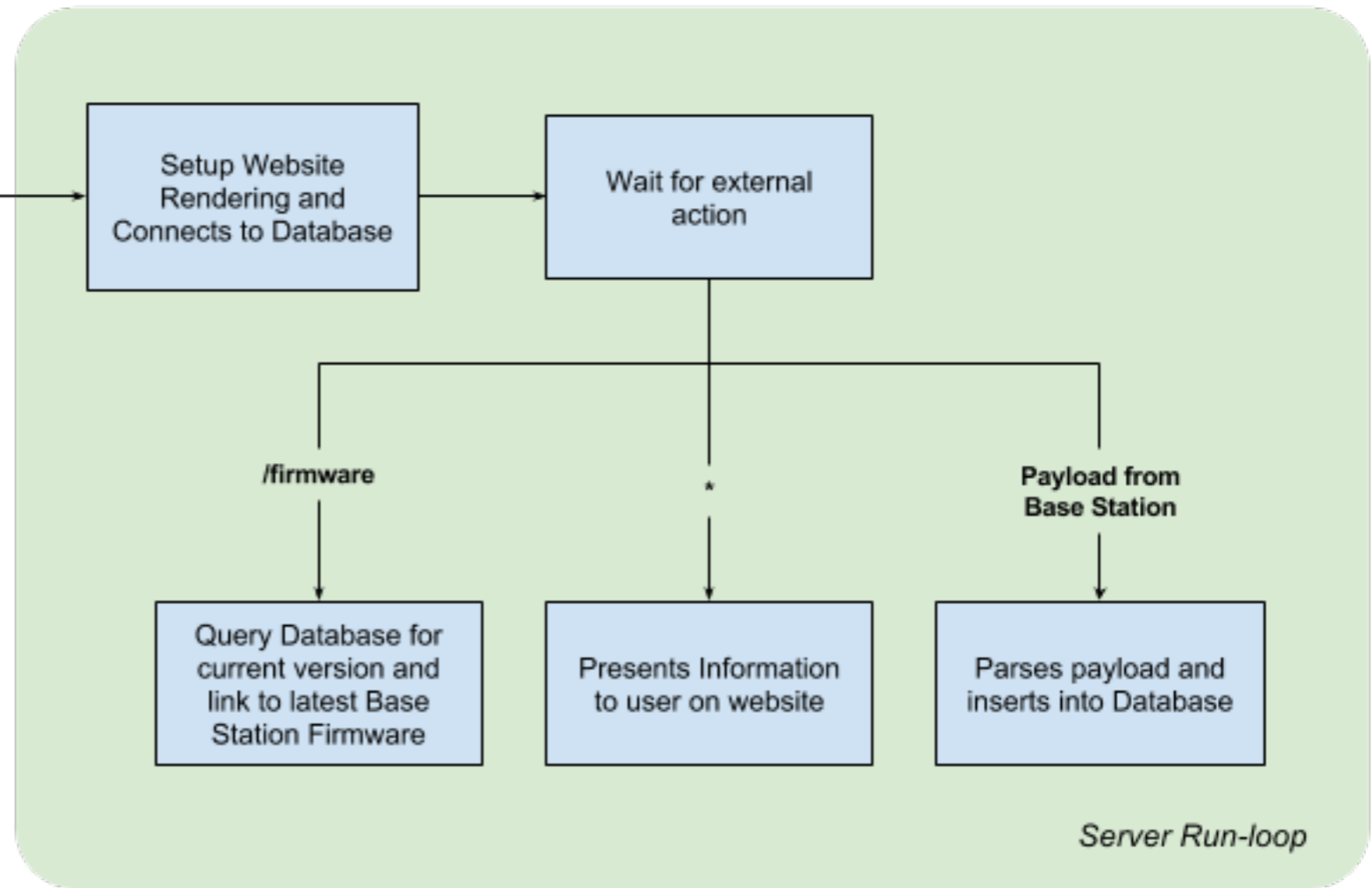
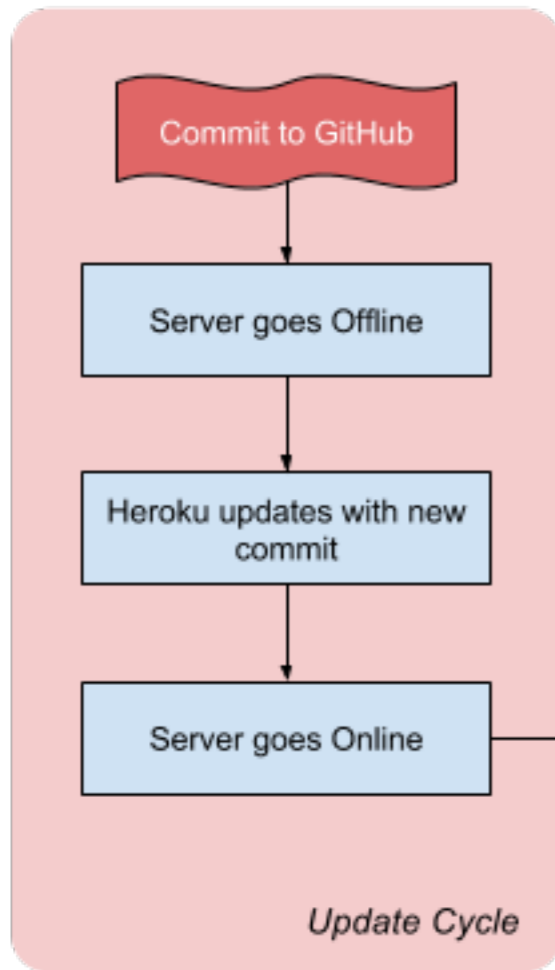


# SERVER REQUIREMENTS

- Accept incoming payloads from Base Stations
- Reliable connection to Base Stations
- Can be updated with new software
- Provide a user interface for the occupancy of each lot
- Ability to store the status of each lot



# SERVER SOFTWARE ARCHITECTURE

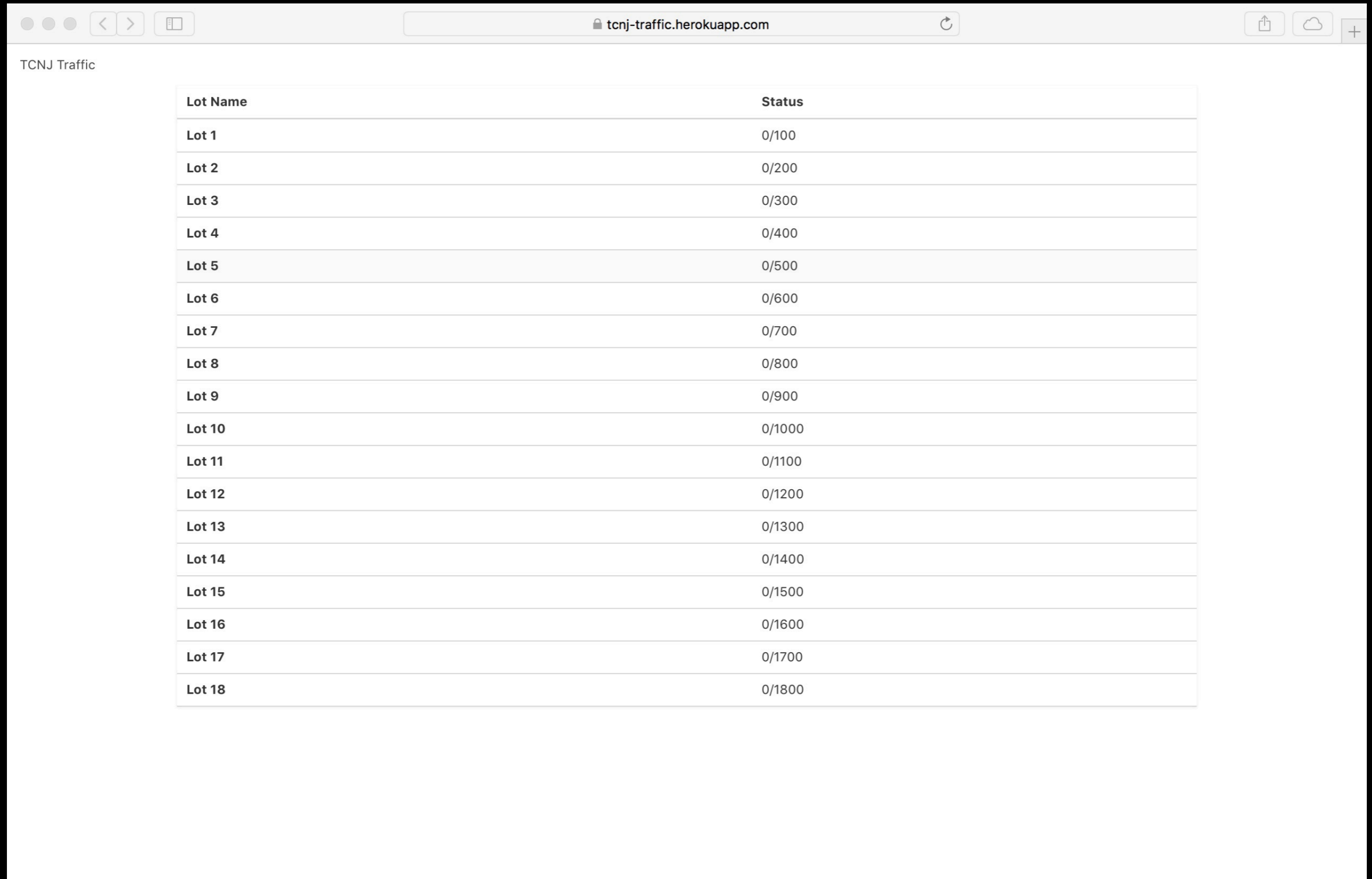


SERVER IS LIVE!

*<https://tcnj-traffic.herokuapp.com>*

# SERVER IS LIVE!

<https://tcnj-traffic.herokuapp.com>



The screenshot shows a web browser window with the address bar displaying "tcnj-traffic.herokuapp.com". The page content is titled "TCNJ Traffic" and features a table with two columns: "Lot Name" and "Status". The table lists 18 lots, each with a status of "0/[total]". The rows are: Lot 1 (0/100), Lot 2 (0/200), Lot 3 (0/300), Lot 4 (0/400), Lot 5 (0/500), Lot 6 (0/600), Lot 7 (0/700), Lot 8 (0/800), Lot 9 (0/900), Lot 10 (0/1000), Lot 11 (0/1100), Lot 12 (0/1200), Lot 13 (0/1300), Lot 14 (0/1400), Lot 15 (0/1500), Lot 16 (0/1600), Lot 17 (0/1700), and Lot 18 (0/1800). The table is styled with a light gray background and thin borders.

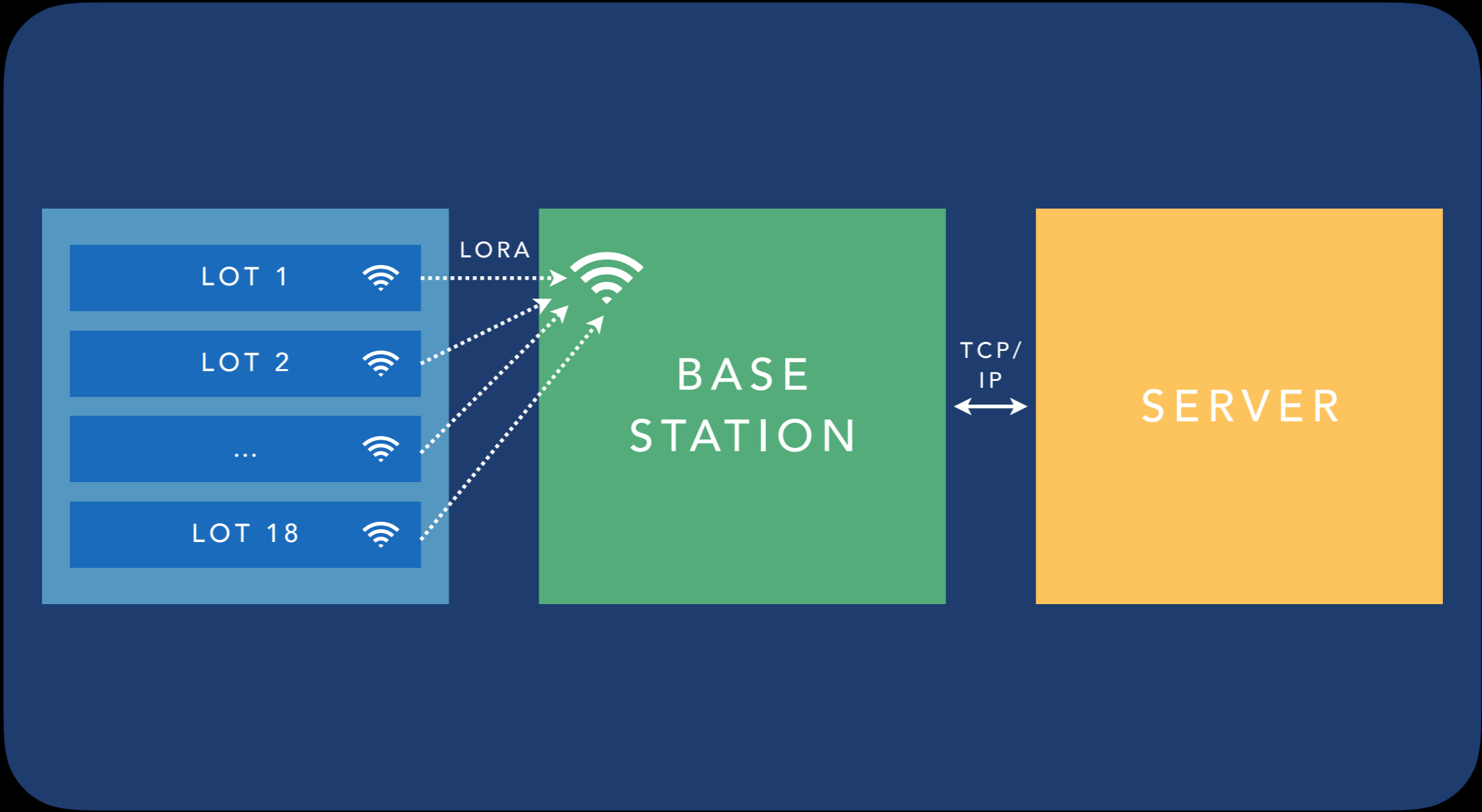
Lot Name	Status
Lot 1	0/100
Lot 2	0/200
Lot 3	0/300
Lot 4	0/400
Lot 5	0/500
Lot 6	0/600
Lot 7	0/700
Lot 8	0/800
Lot 9	0/900
Lot 10	0/1000
Lot 11	0/1100
Lot 12	0/1200
Lot 13	0/1300
Lot 14	0/1400
Lot 15	0/1500
Lot 16	0/1600
Lot 17	0/1700
Lot 18	0/1800

# UPCOMING SERVER DEVELOPMENT TASKS

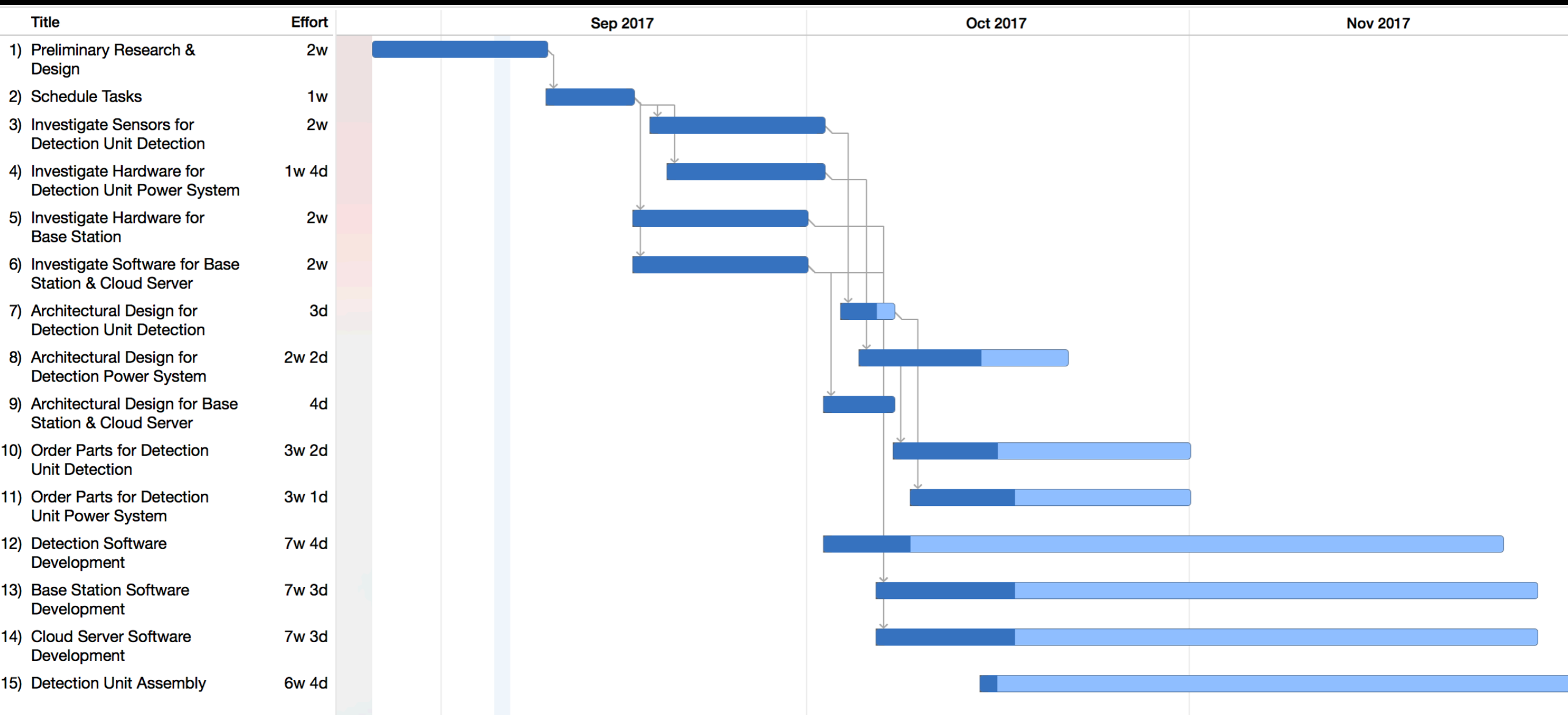
1	ADD STYLESHEETS TO FRONTEND OCCUPANCY SITE
2	ADD ADMINISTRATIVE ACCESS
3	ADD ABILITY TO VIEW AND MANAGE DETECTION UNITS
4	ADD ABILITY TO VIEW AND MANAGE CONNECTED BASE STATIONS



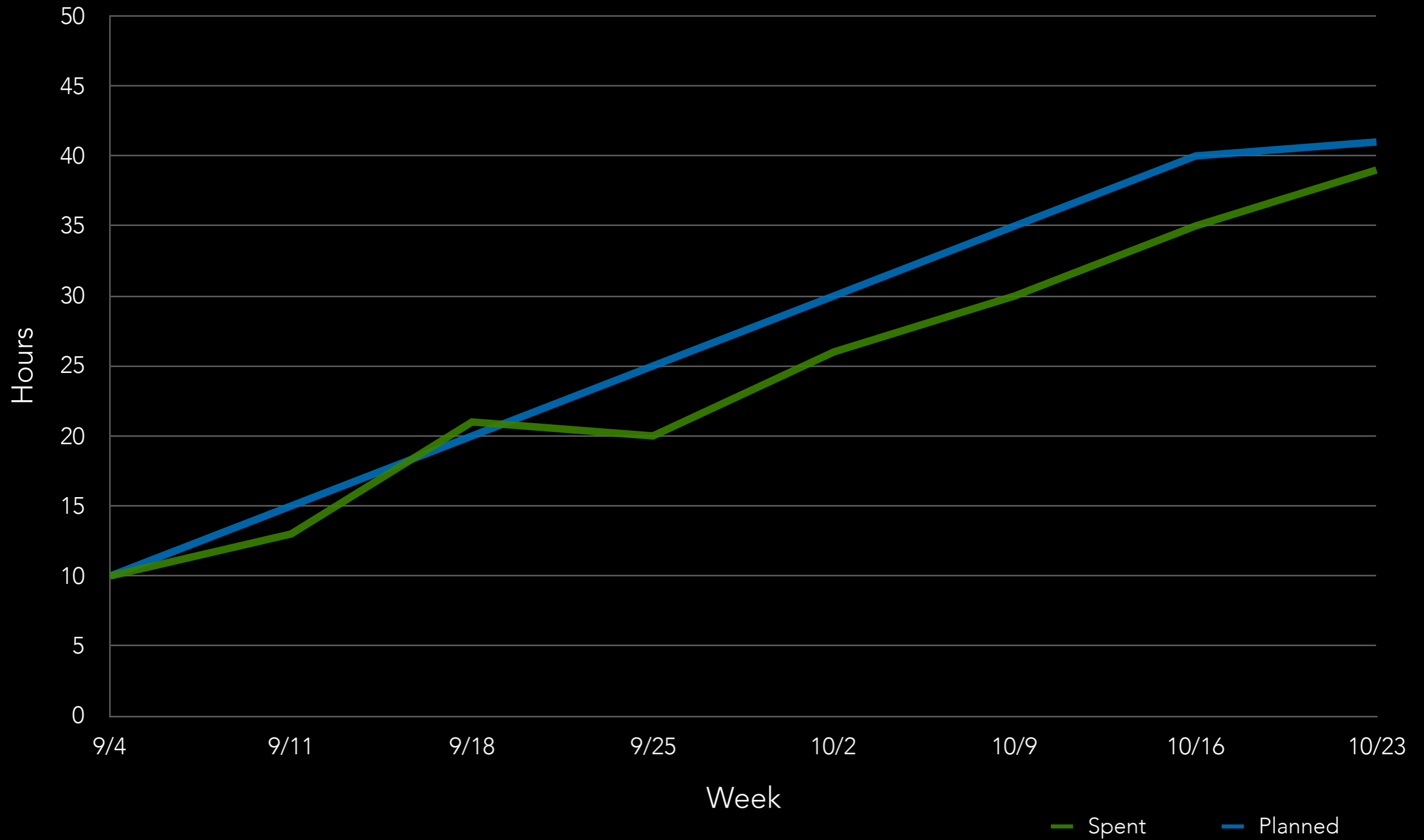
# SENIOR PROJECT ARCHITECTURE



# SCHEDULE



# PROJECTED TIME BUDGET



# PROJECTED COST BUDGET

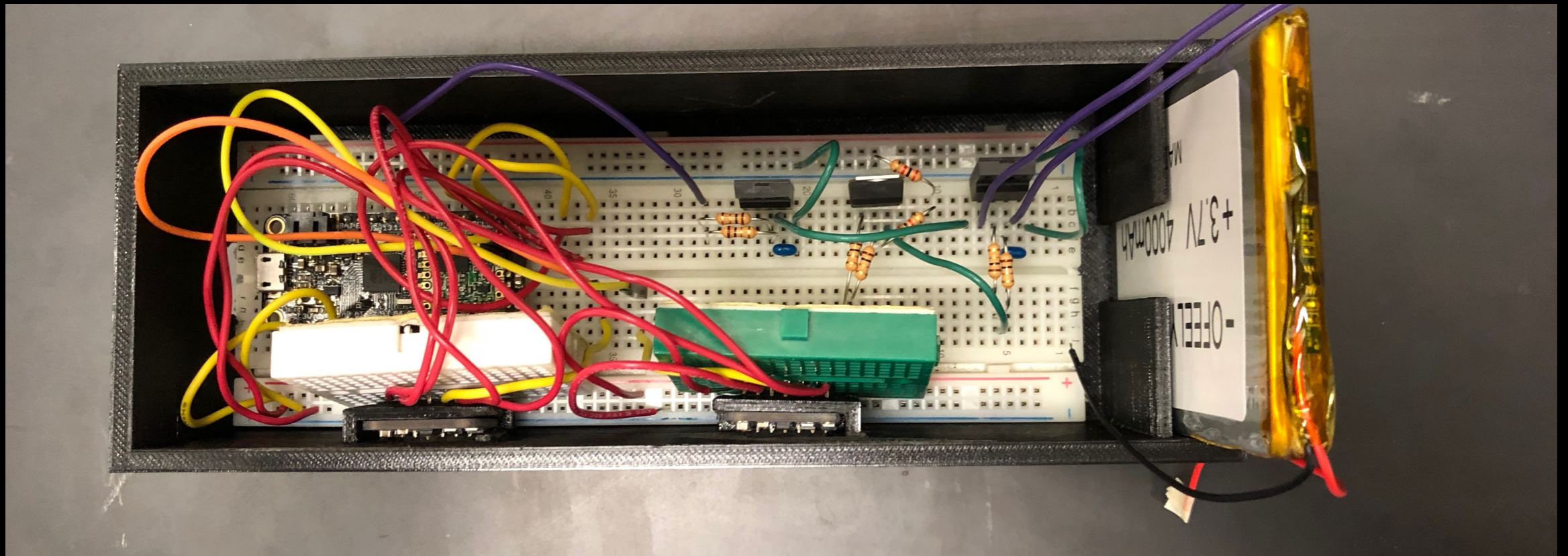


# PROJECTED COST BUDGET

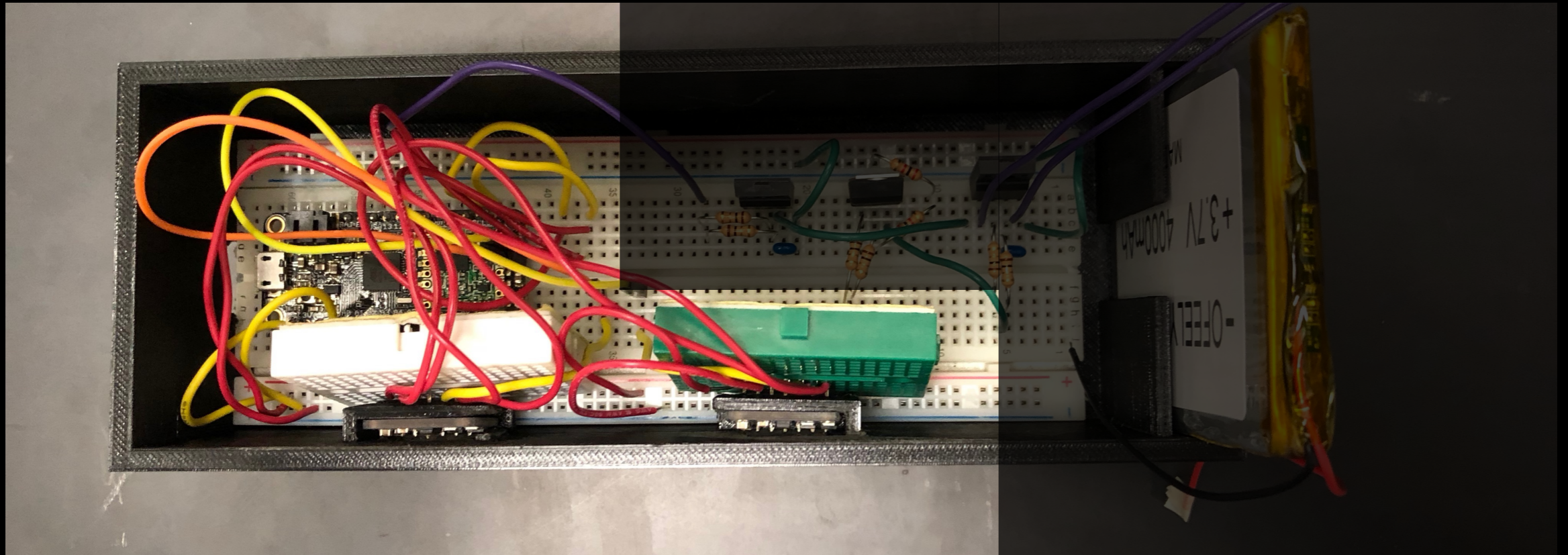
ITEM	QUANTITY	COST PER QUANTITY	TOTAL COST
ADAFRUIT FEATHER	2	\$35	\$70
RASPBERRY PI	1	\$32	\$35
LIDAR SENSORS	2	\$15	\$30
LIPO BATTERY	1	\$17	\$17
SCHOTTKY DIODES	2	\$1.35	\$2.70
LINEAR VOLTAGE REG	1	\$0.53	\$0.53
(DC/DC CONVERTER)	1	\$0.47	\$0.47
BUDGET			\$400.00
GRAND TOTAL			\$155.70
REMAINING FUNDS			\$244.30

DETECTION UNIT  
PROTOTYPE

# DETECTION UNIT PROTOTYPE

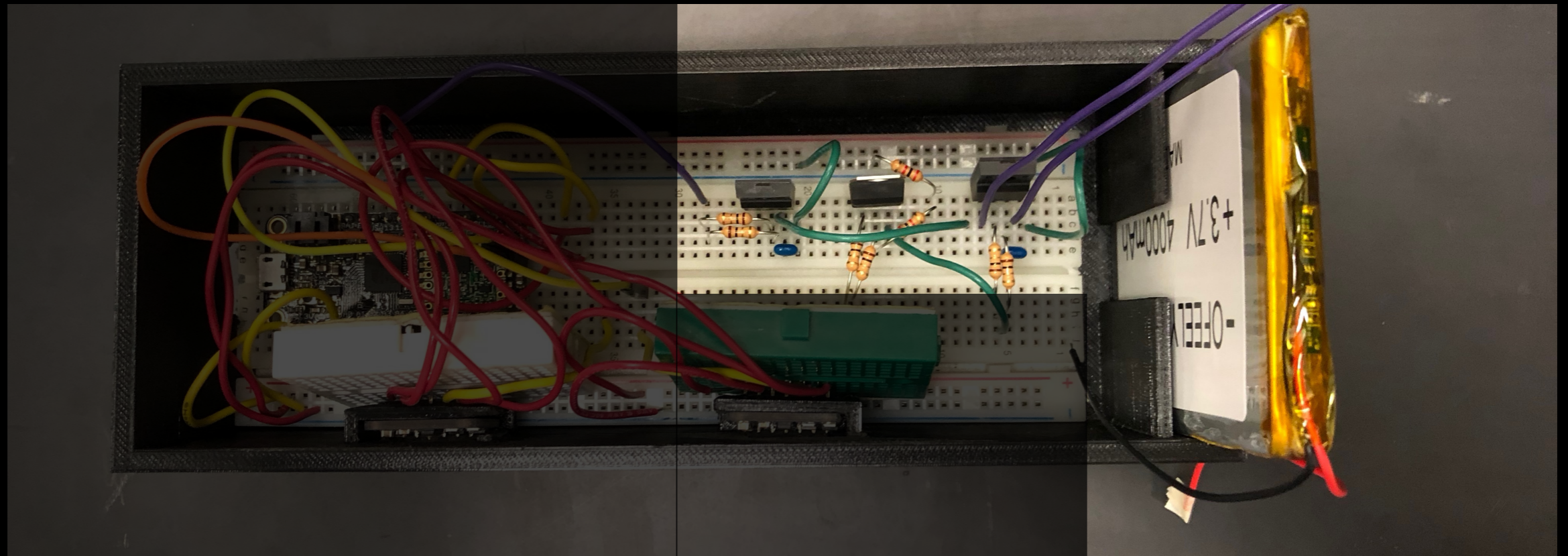


# DETECTION UNIT CORE PROTOTYPE





# POWER SUBSYSTEM PROTOTYPE





# Parking Lot Availability System using a Campus- Wide Wireless Network

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STEPHANIE FOURNIER,  
WARREN SETO

ADVISOR: DR. PEARLSTEIN